

FIG. 6-1  
V COUNTER

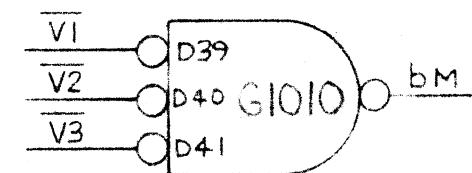
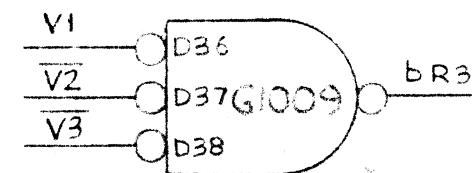
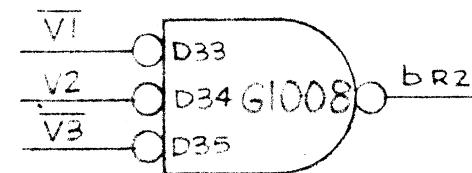
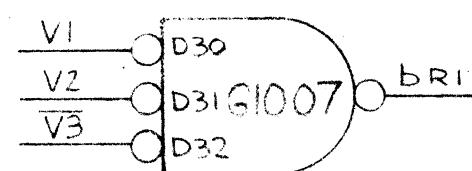
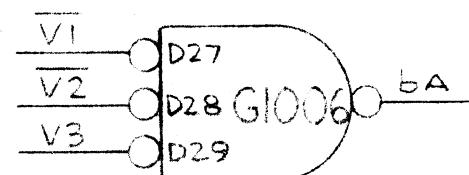
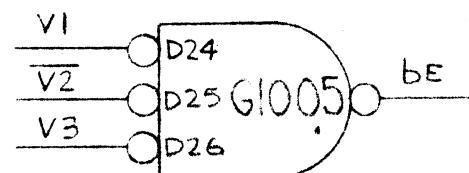
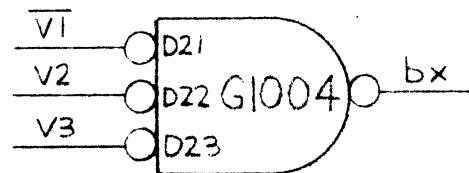
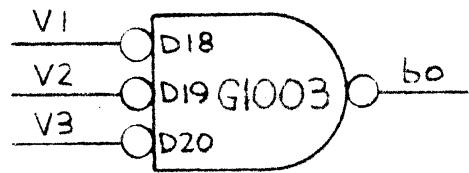


FIG 6-2  
V COUNT DECODER

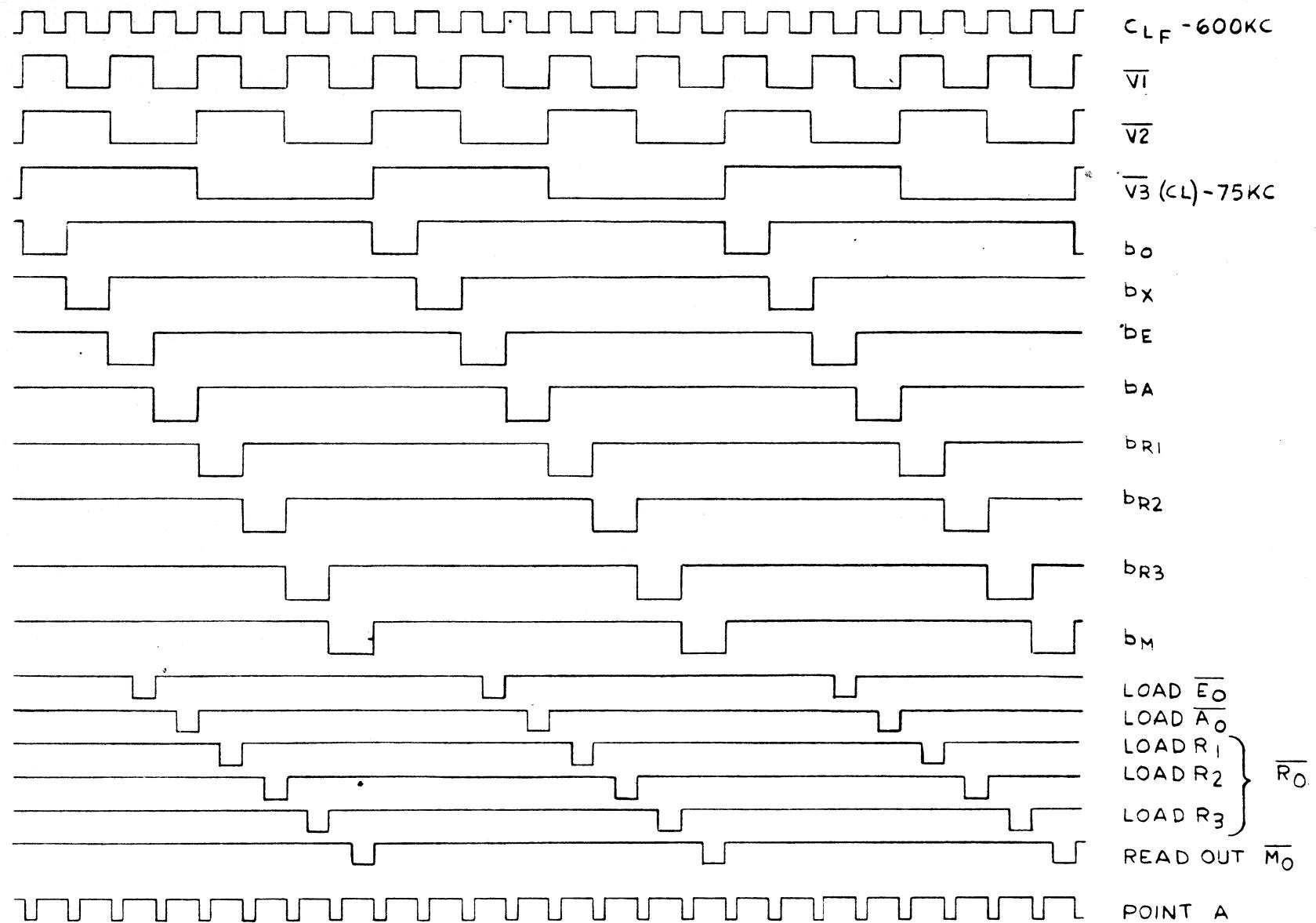


FIG. 6-3  
SUB-BIT TIMING

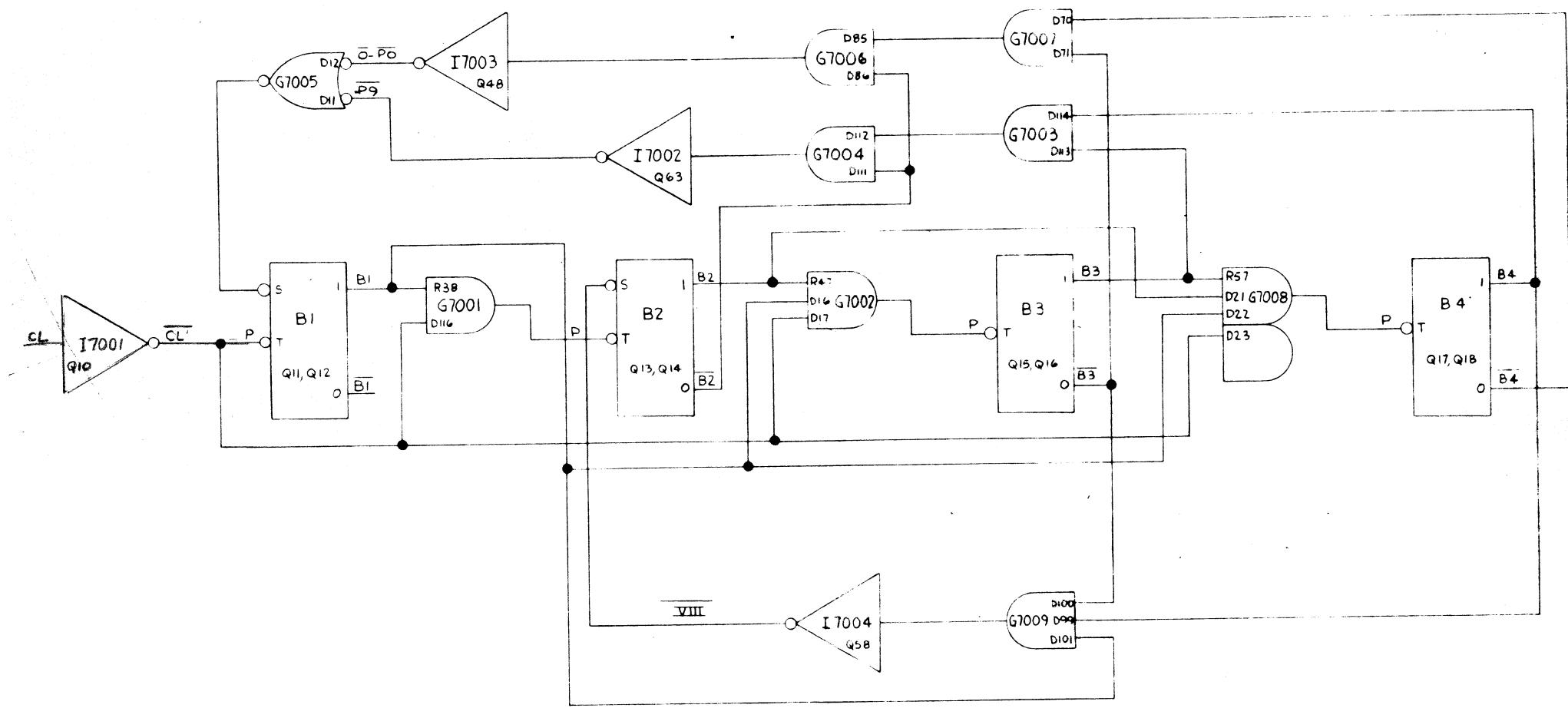


FIG. 6-4  
BIT COUNTER

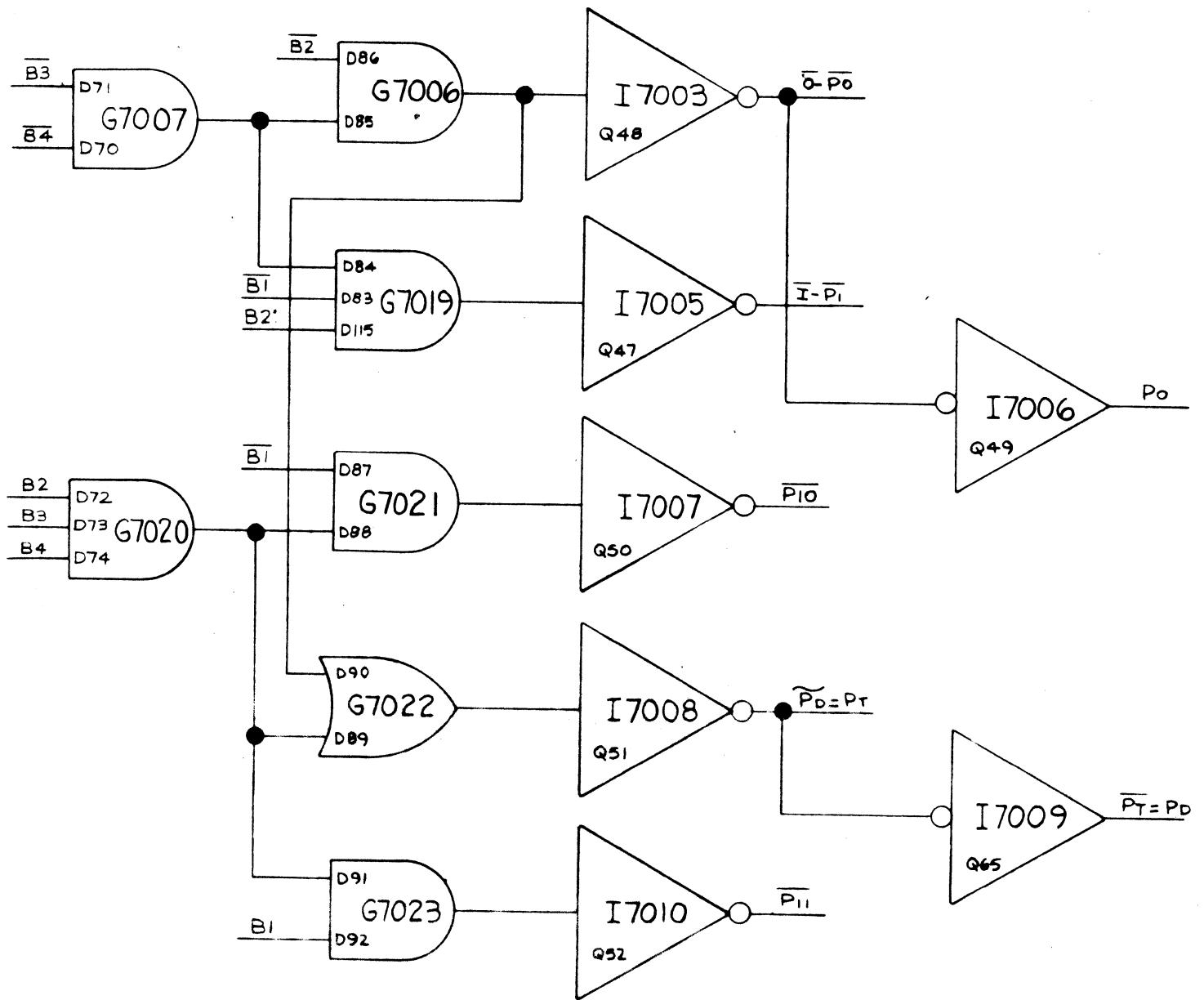


FIG. 6-5  
BIT COUNT DECODER  
(SHT. 1 of 2)

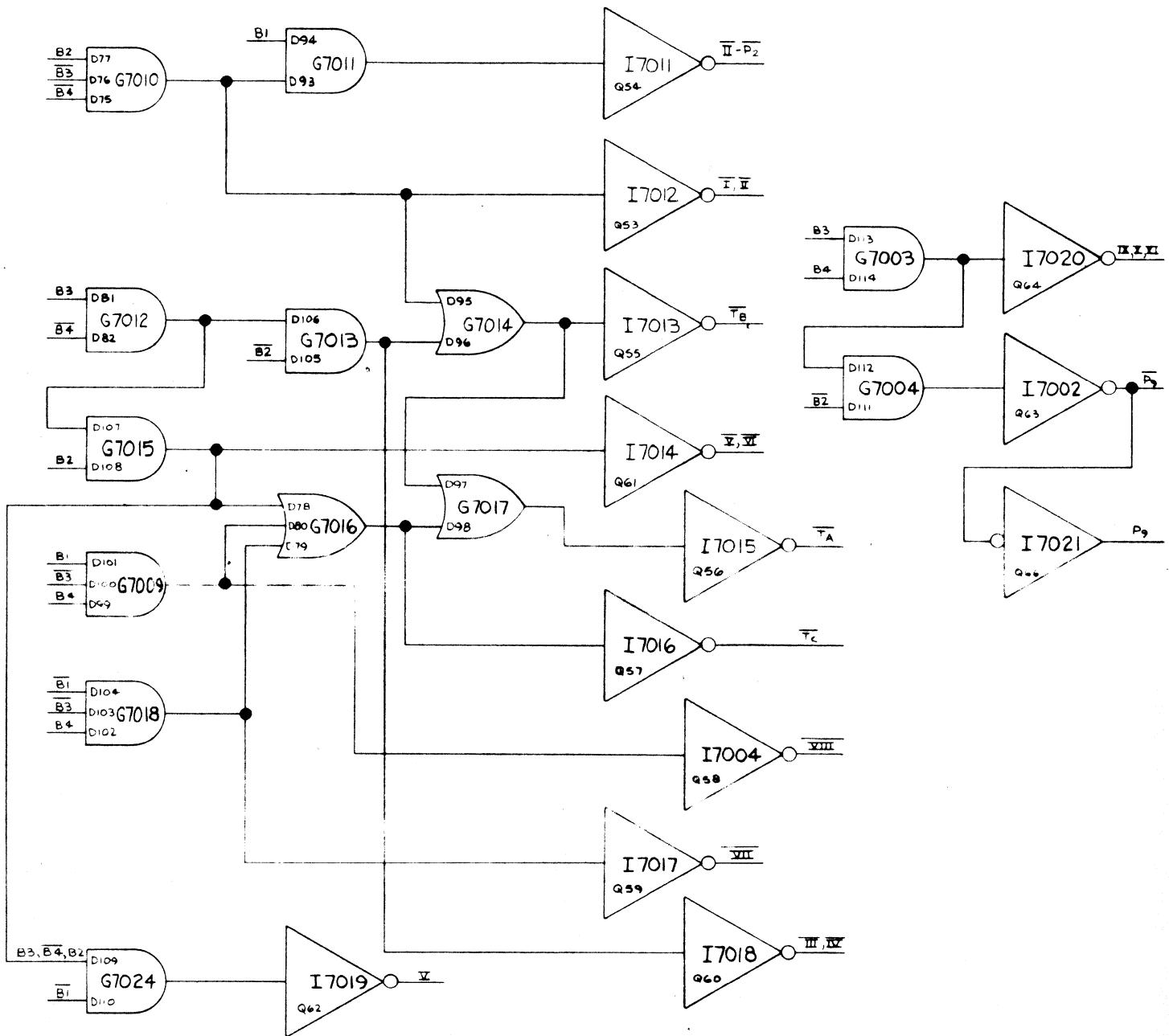


FIG. 6-5  
BIT COUNT DECODER

(SHT 2 OF 2)

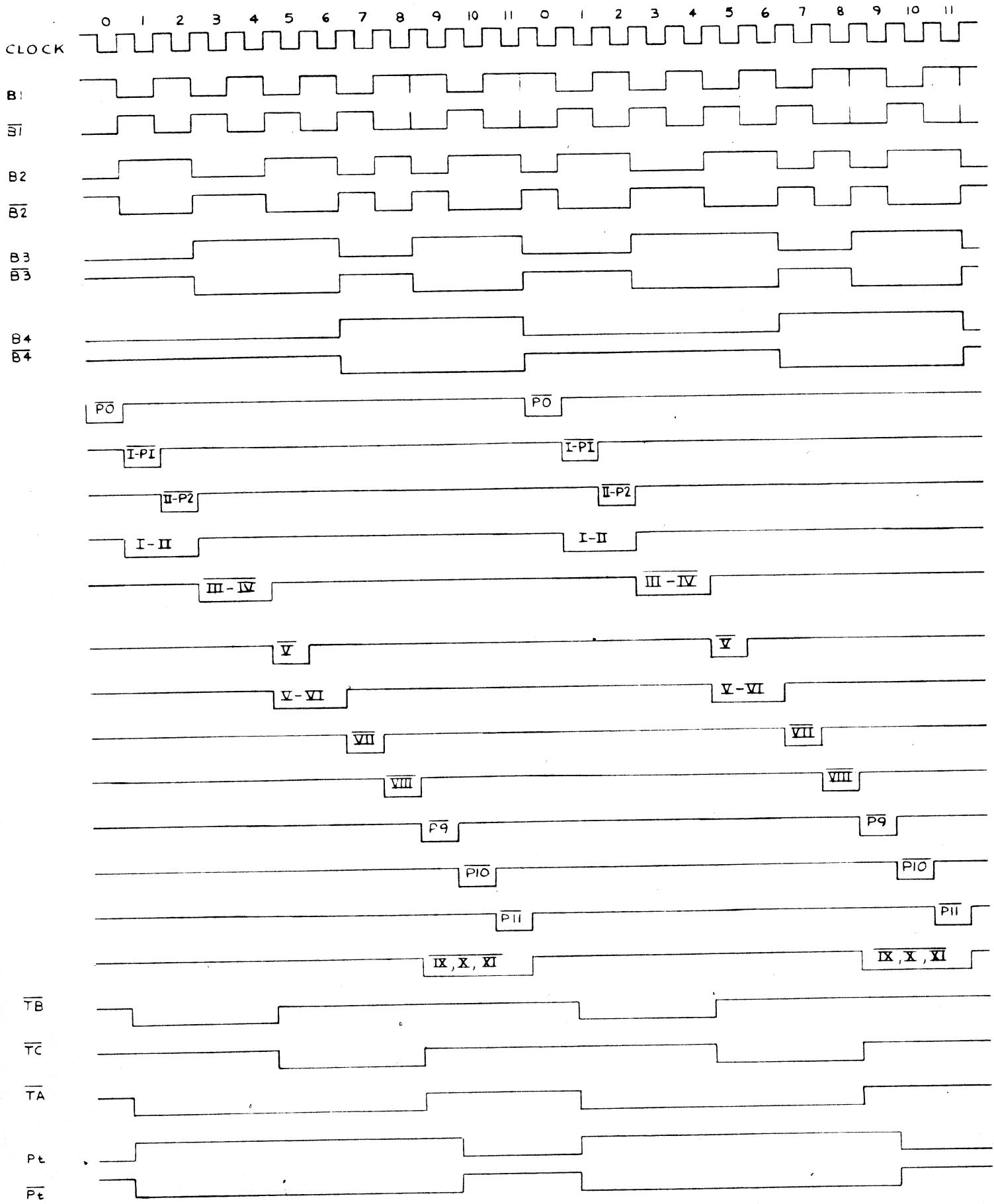


FIG. 6-6  
BIT TIMING

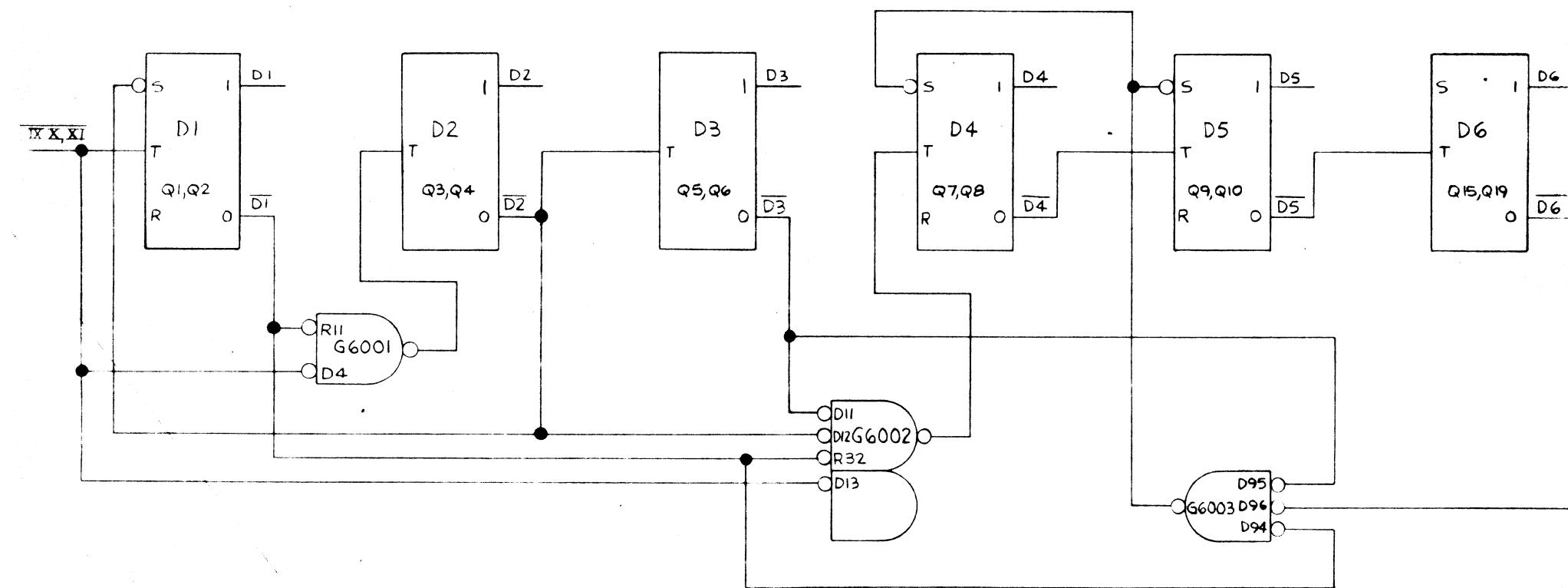


FIG. 6-7  
DIGIT COUNTER

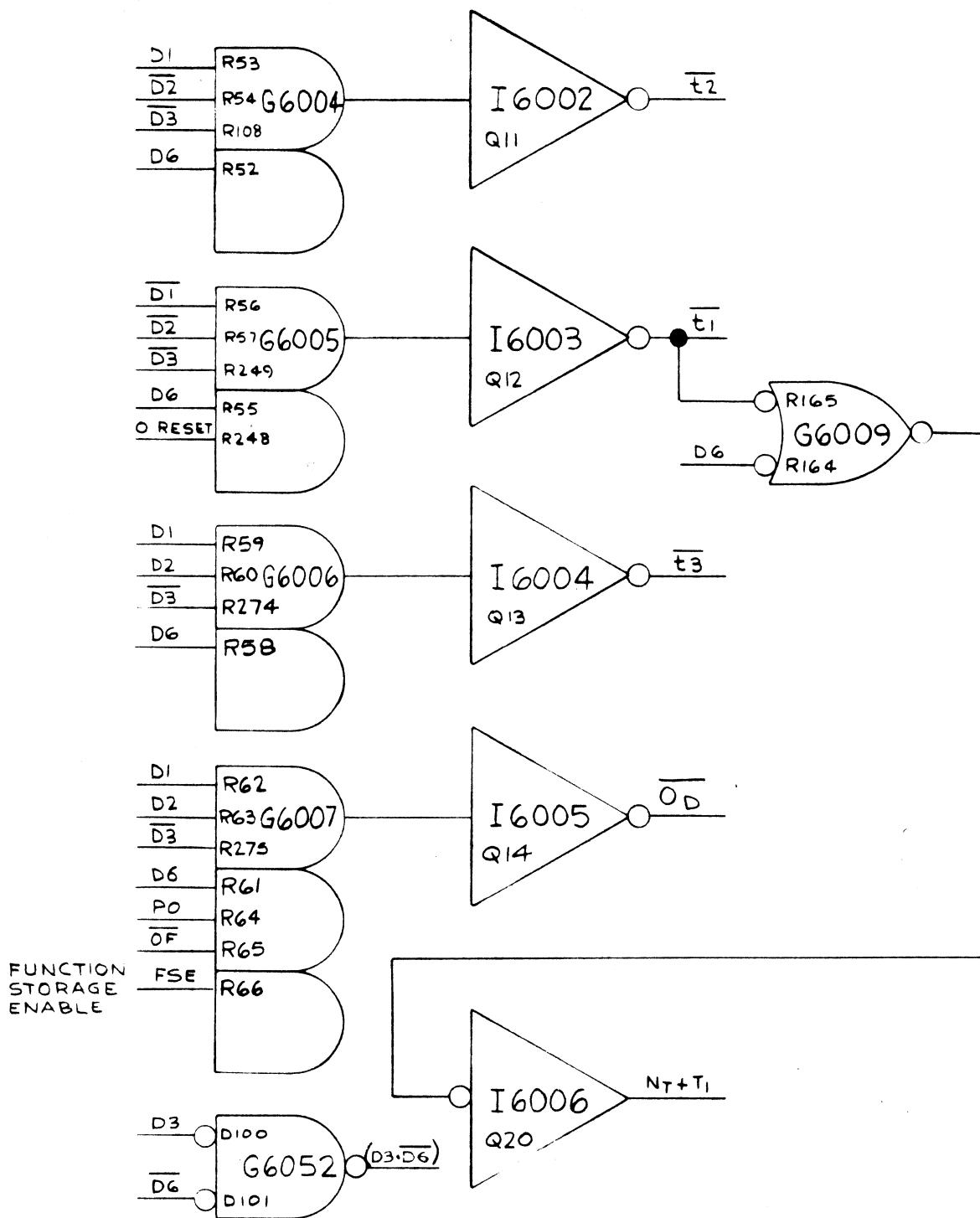


FIG. 6-8  
DIGIT COUNT DECODER

(SHEET 1 OF 2)

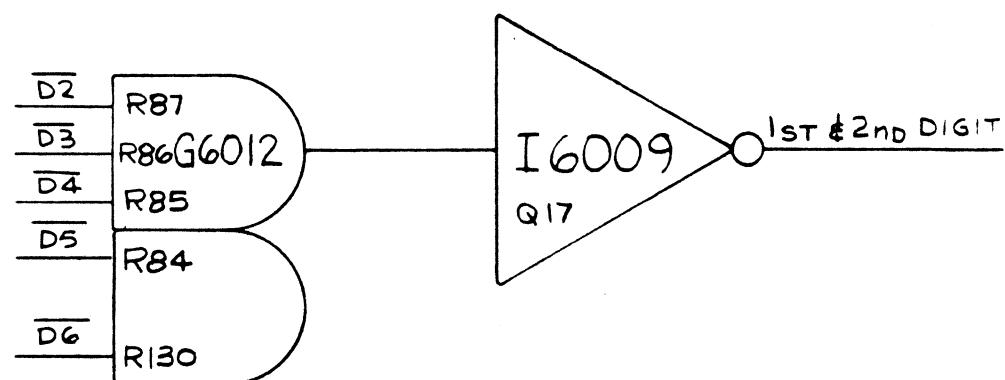
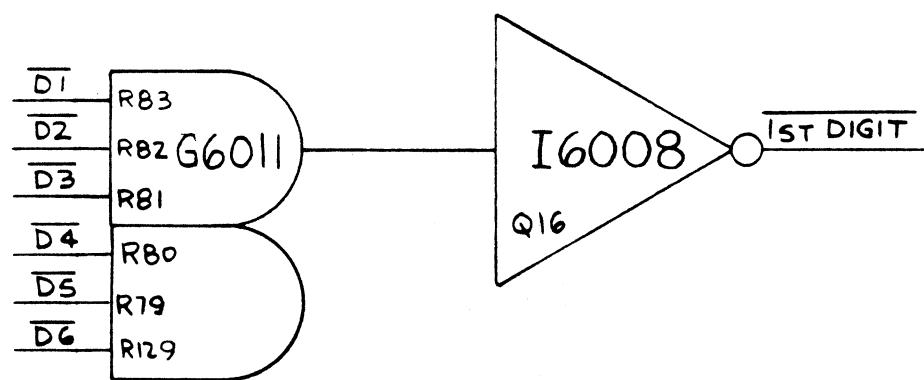
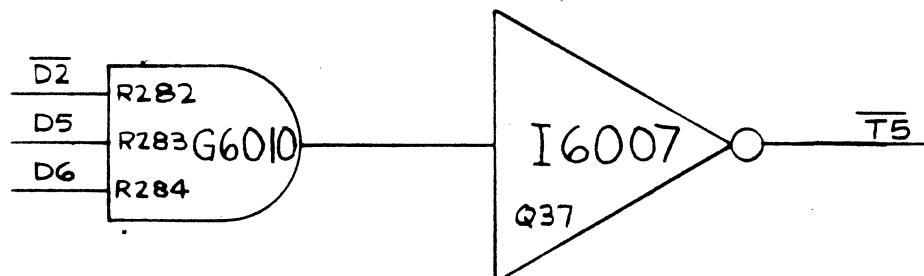
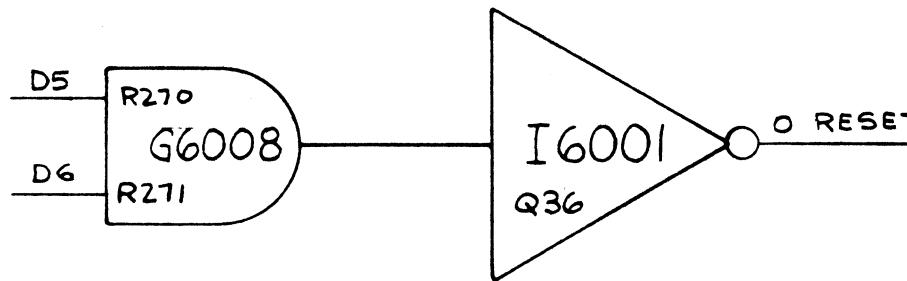


FIG. 6-8  
DIGIT COUNT DECODER

(SHEET 2 OF 2)

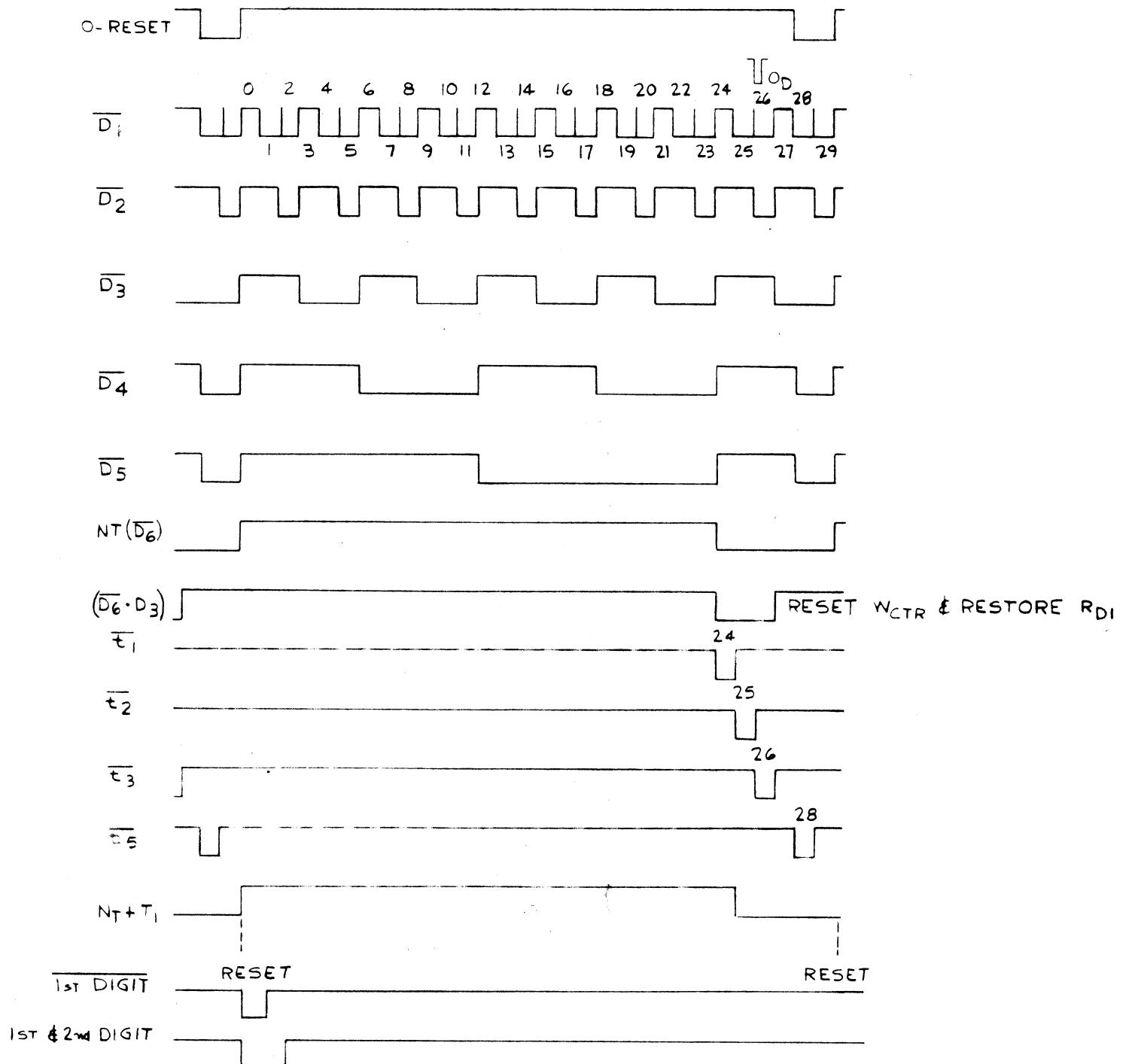


FIG. 6-9  
DIGIT TIMING

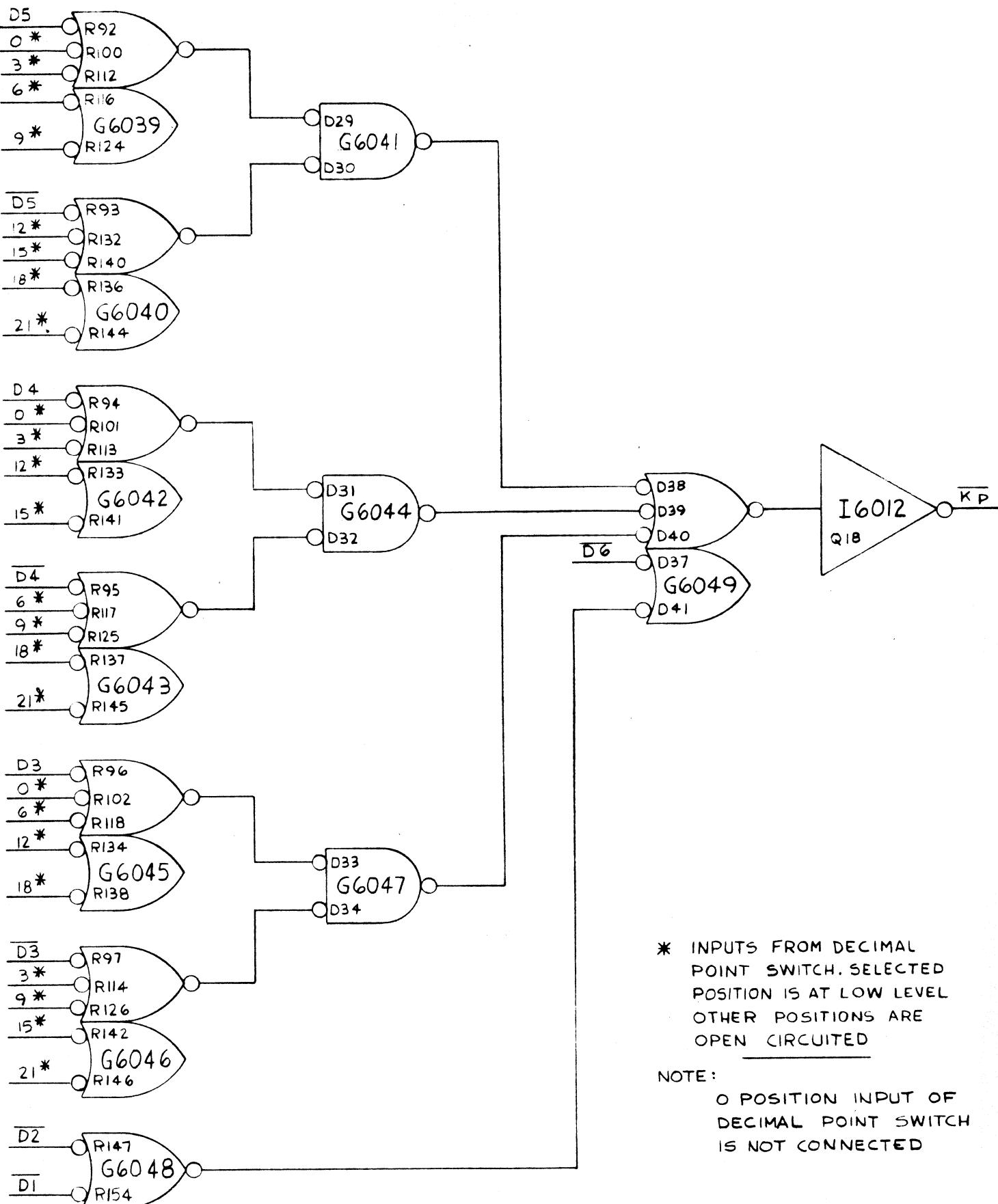


FIG. 6-10  
Kp LOGIC

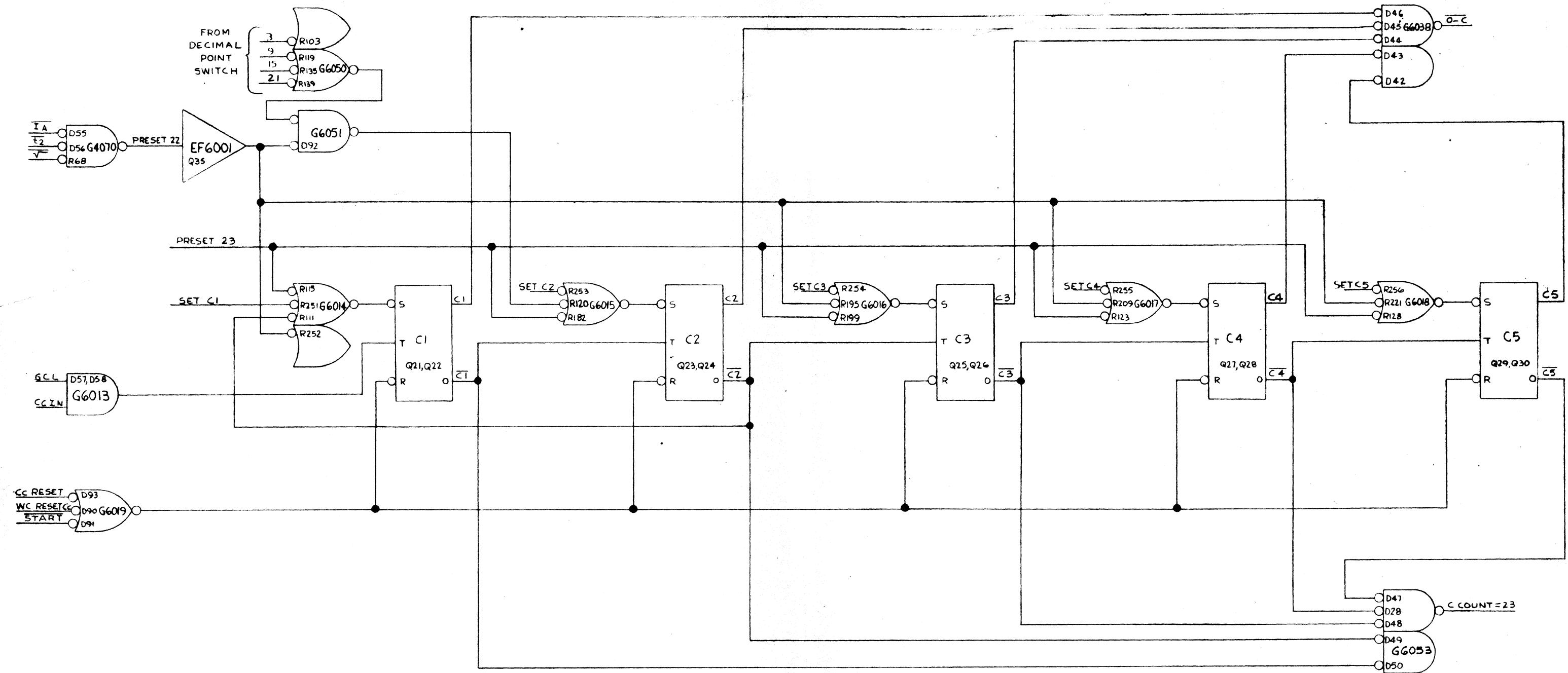


FIG 6-11  
C COUNTER

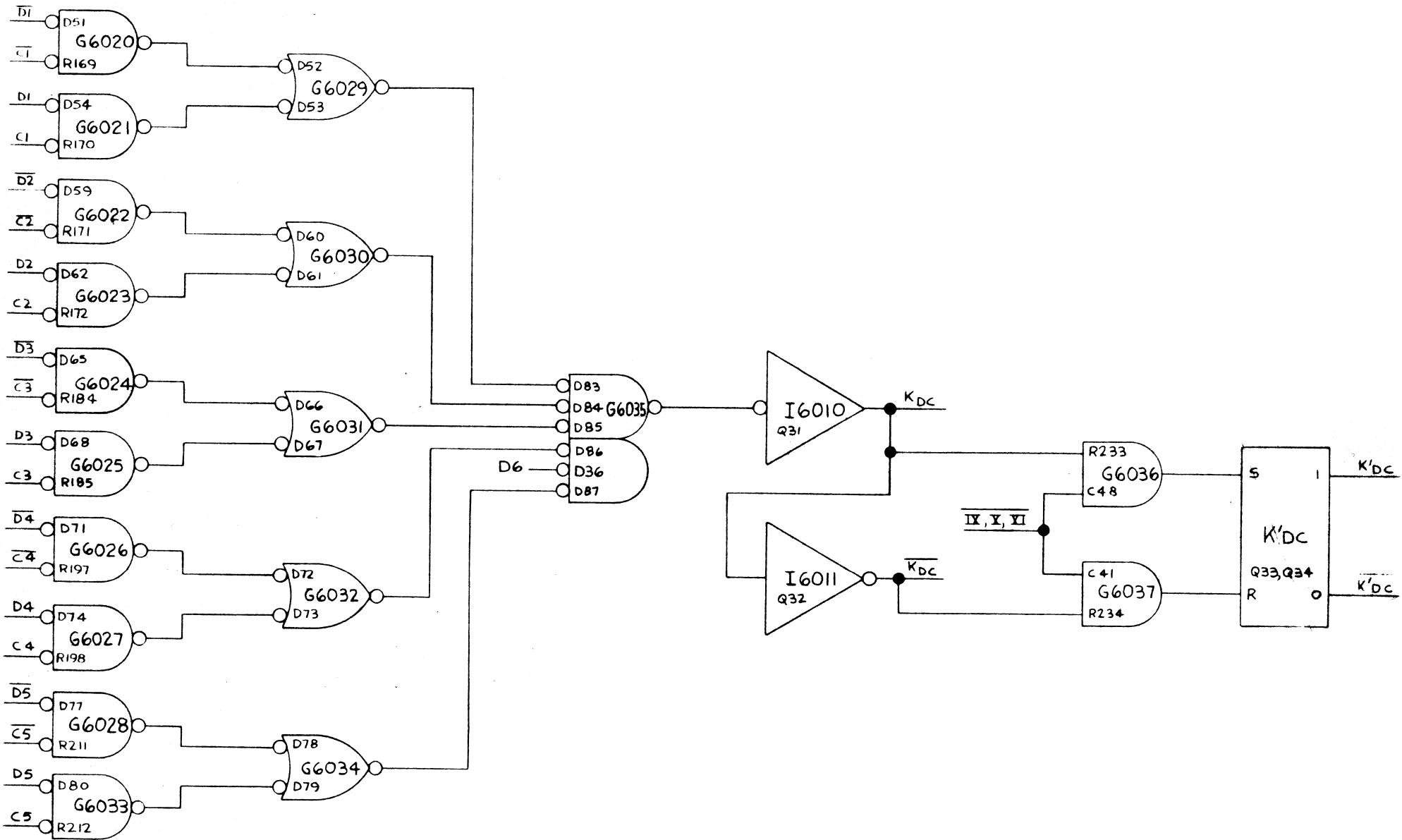


FIG. 6-12  
 $K_{DC}, K'_{DC}$  LOGIC

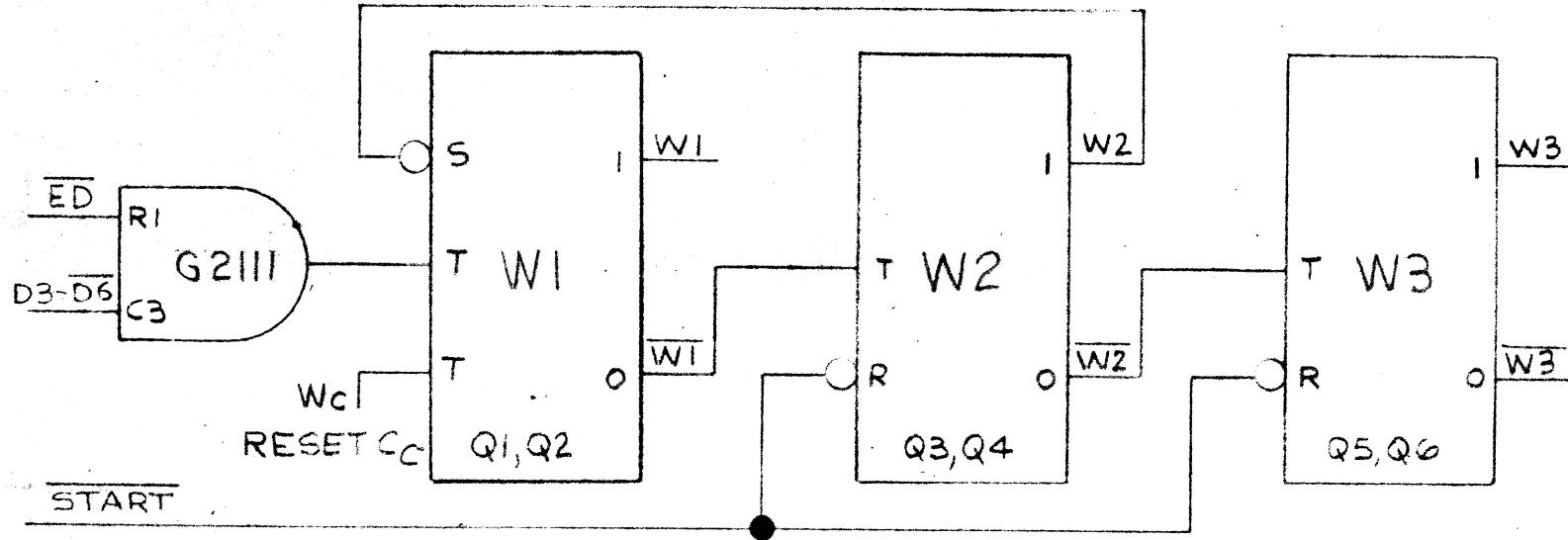


FIG. 6-13  
WORD COUNTER

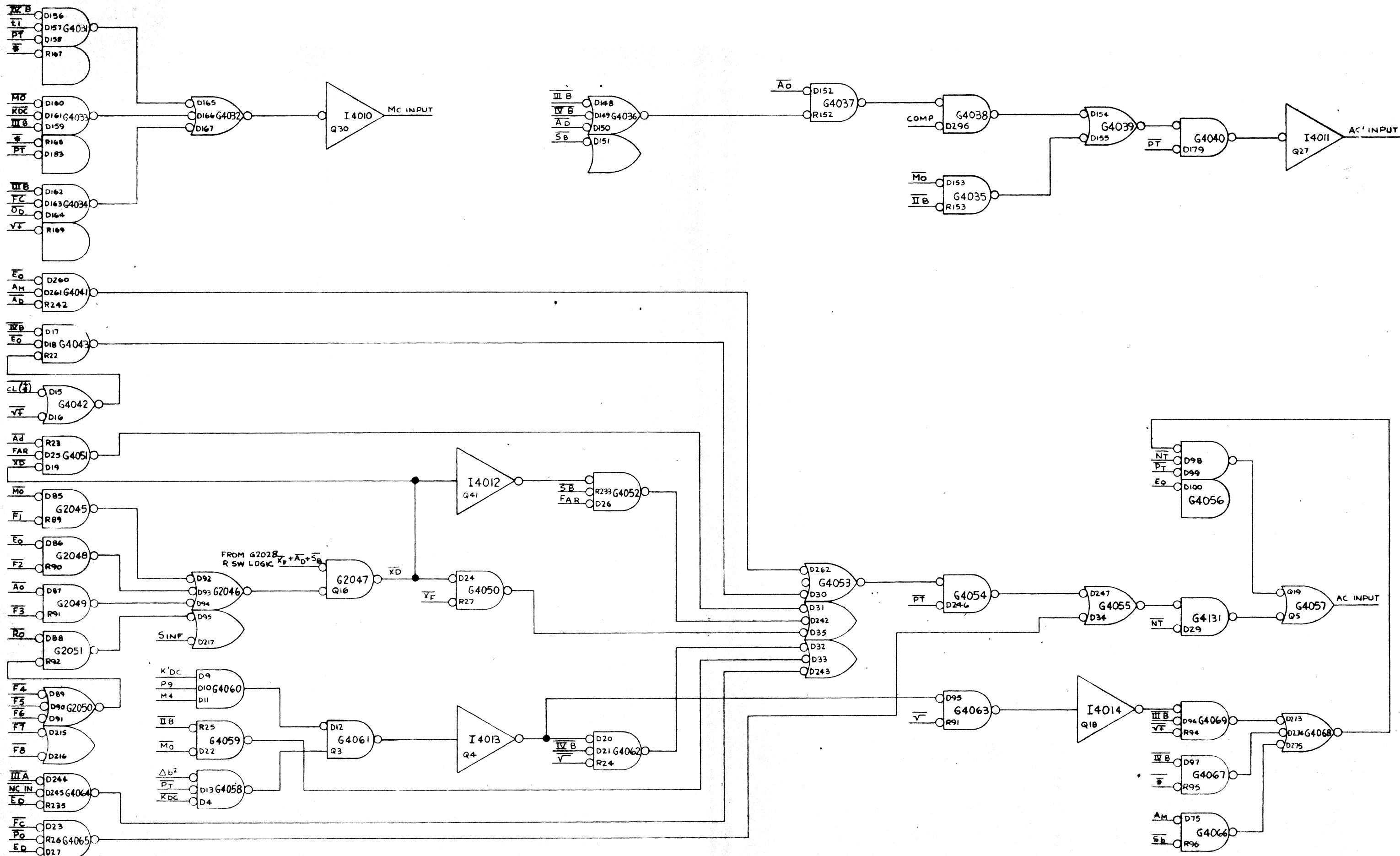


FIG. 6-14: A COUNTER AND M COUNTER AC IN, AC' IN AND MC IN GATES

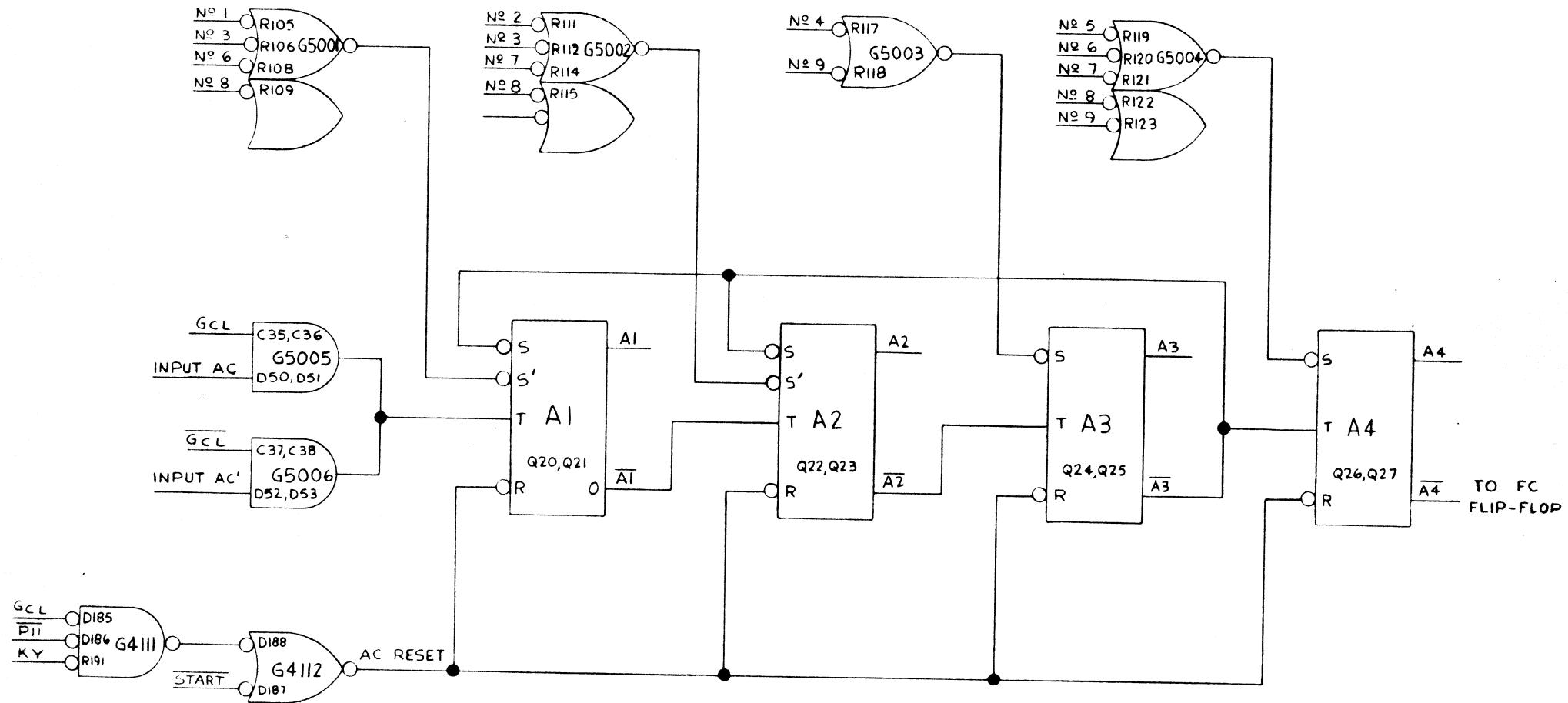


FIG. 6-15  
A COUNTER

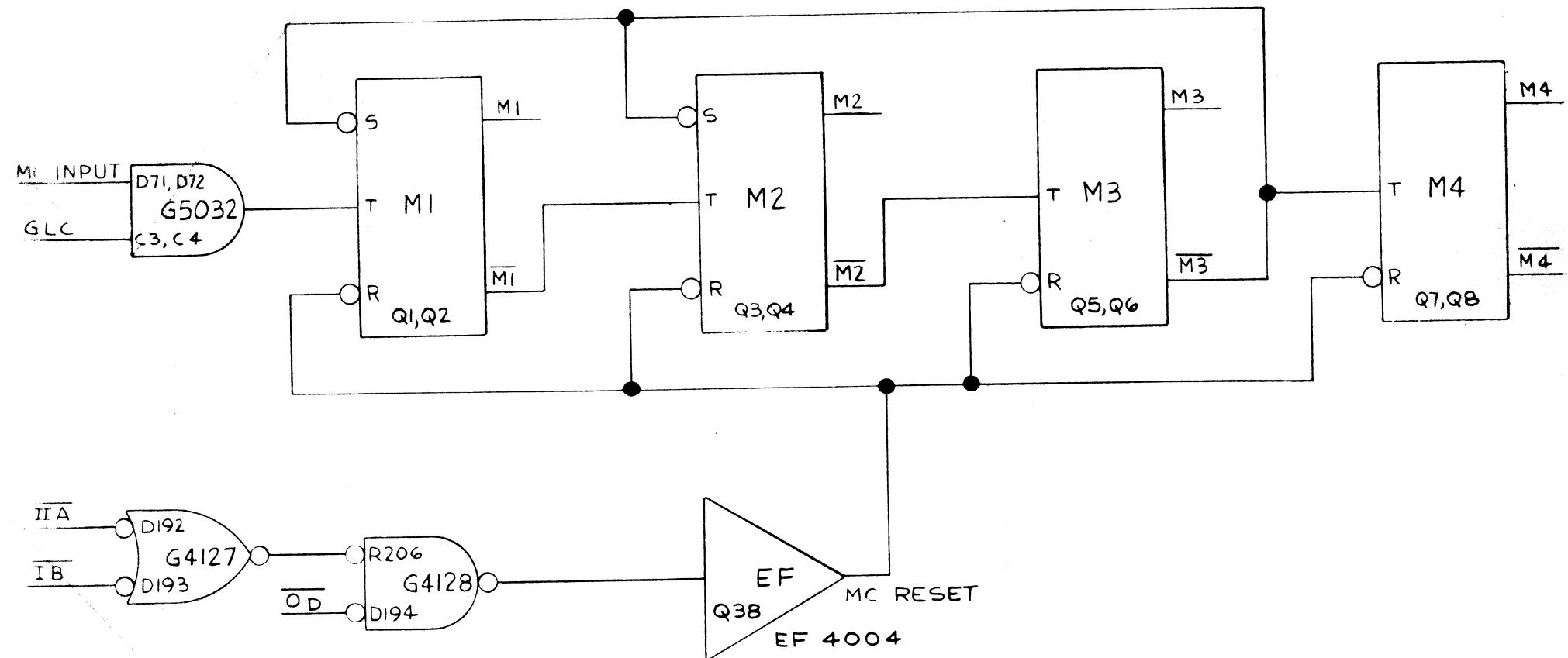


FIG. 6-16

M COUNTER

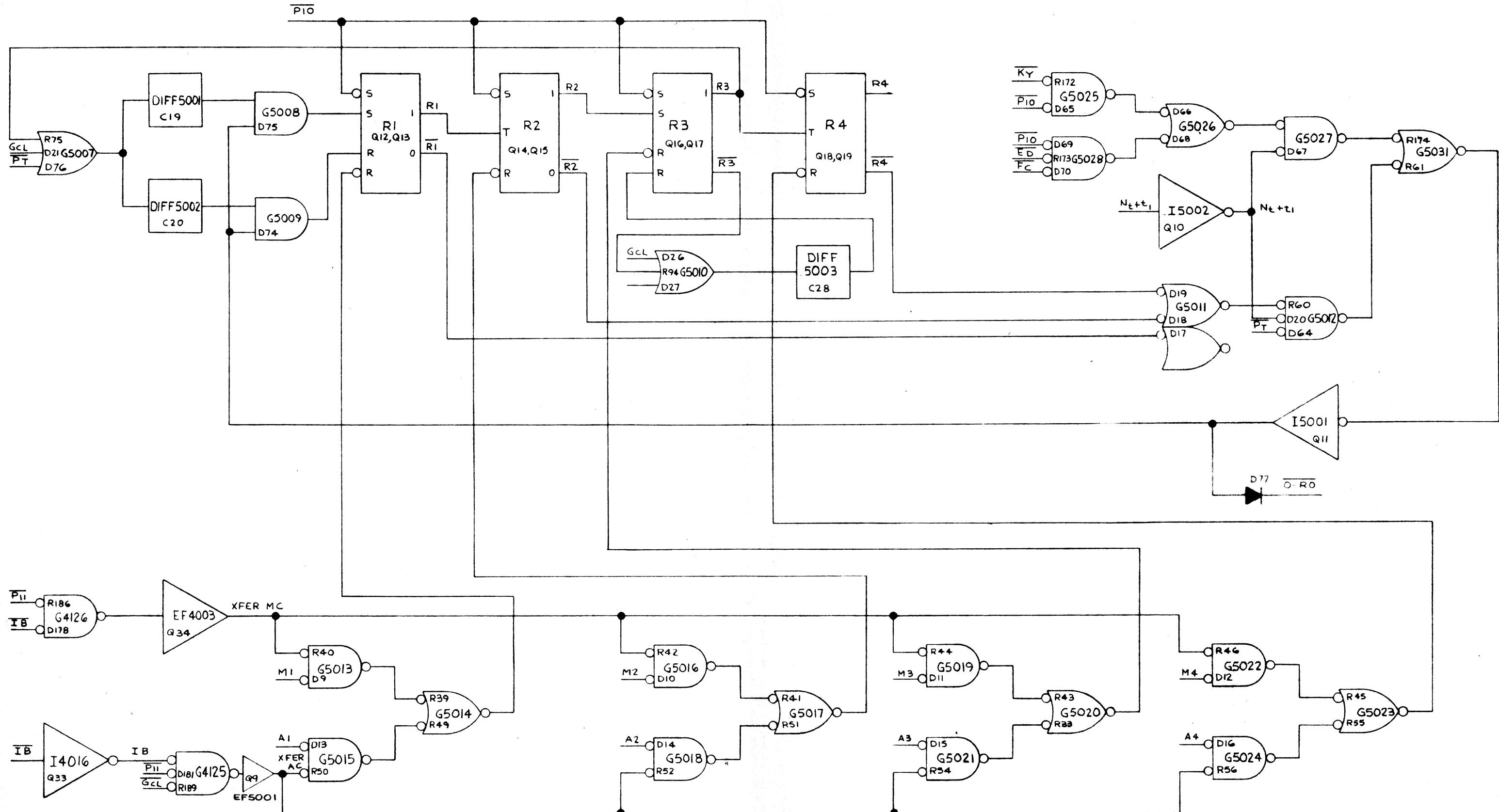


FIG 6-17  
R COUNTER

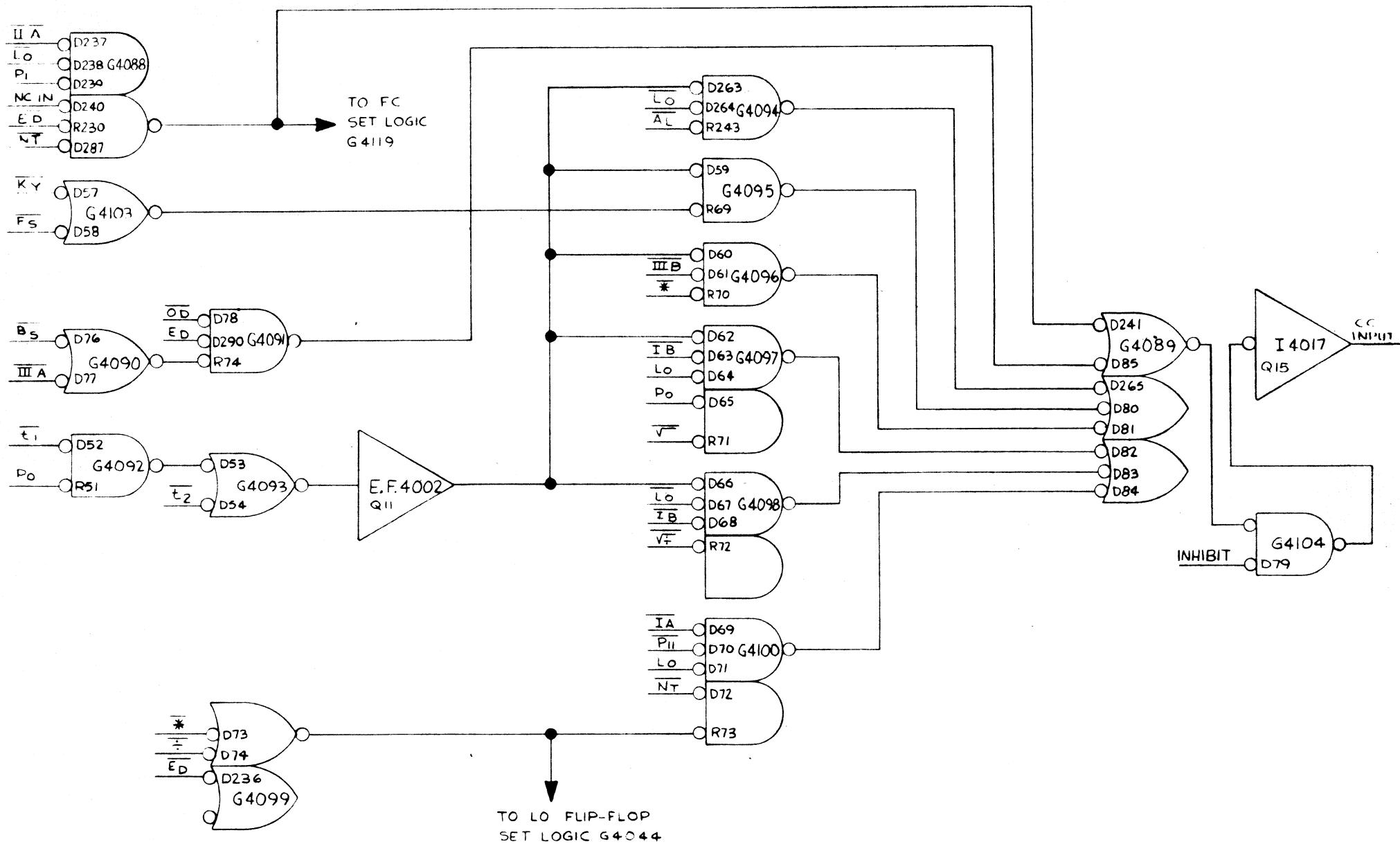


FIG.6-18: C COUNTER INPUT GATES

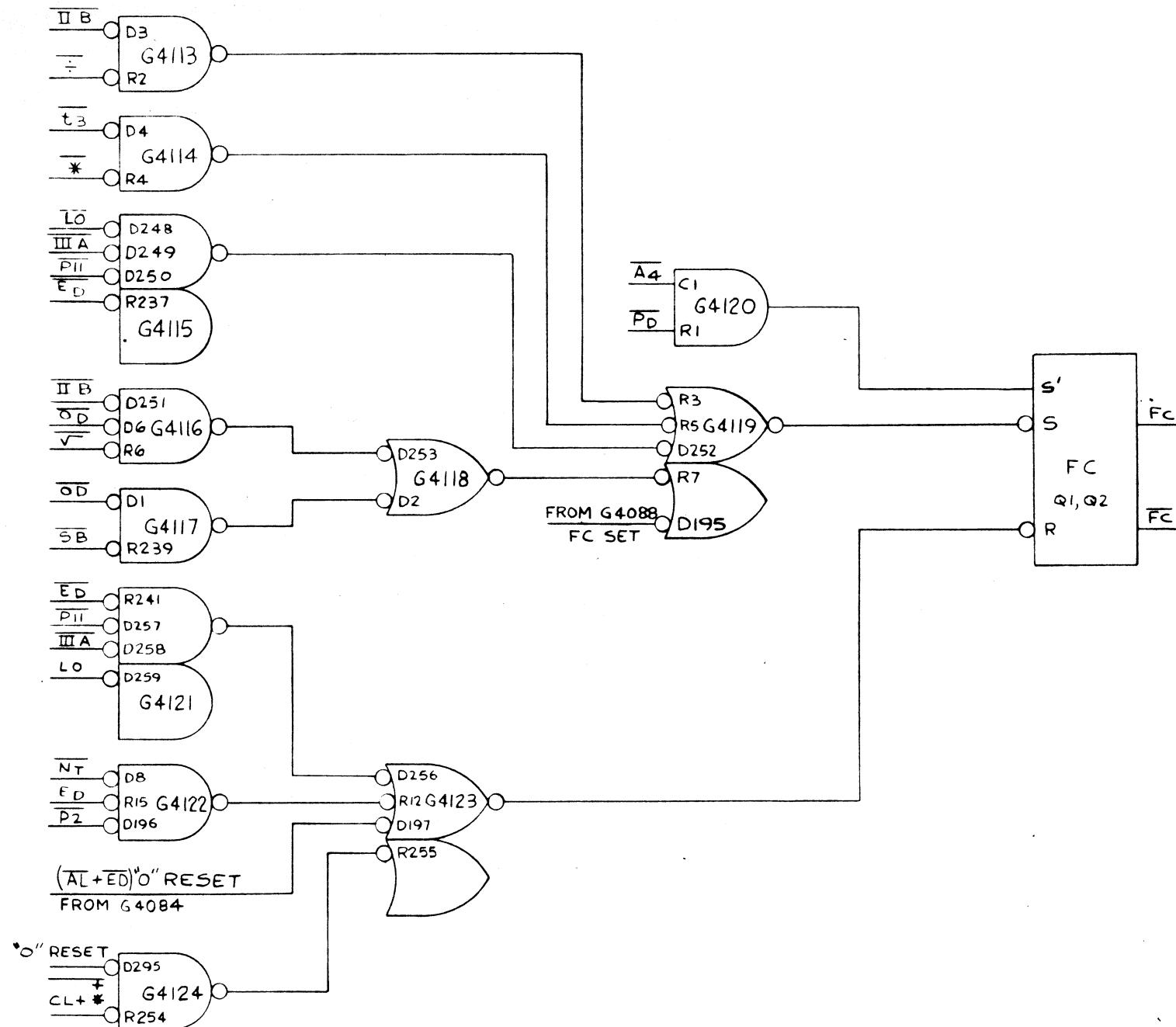


FIG. 6-10. EC 1.02M

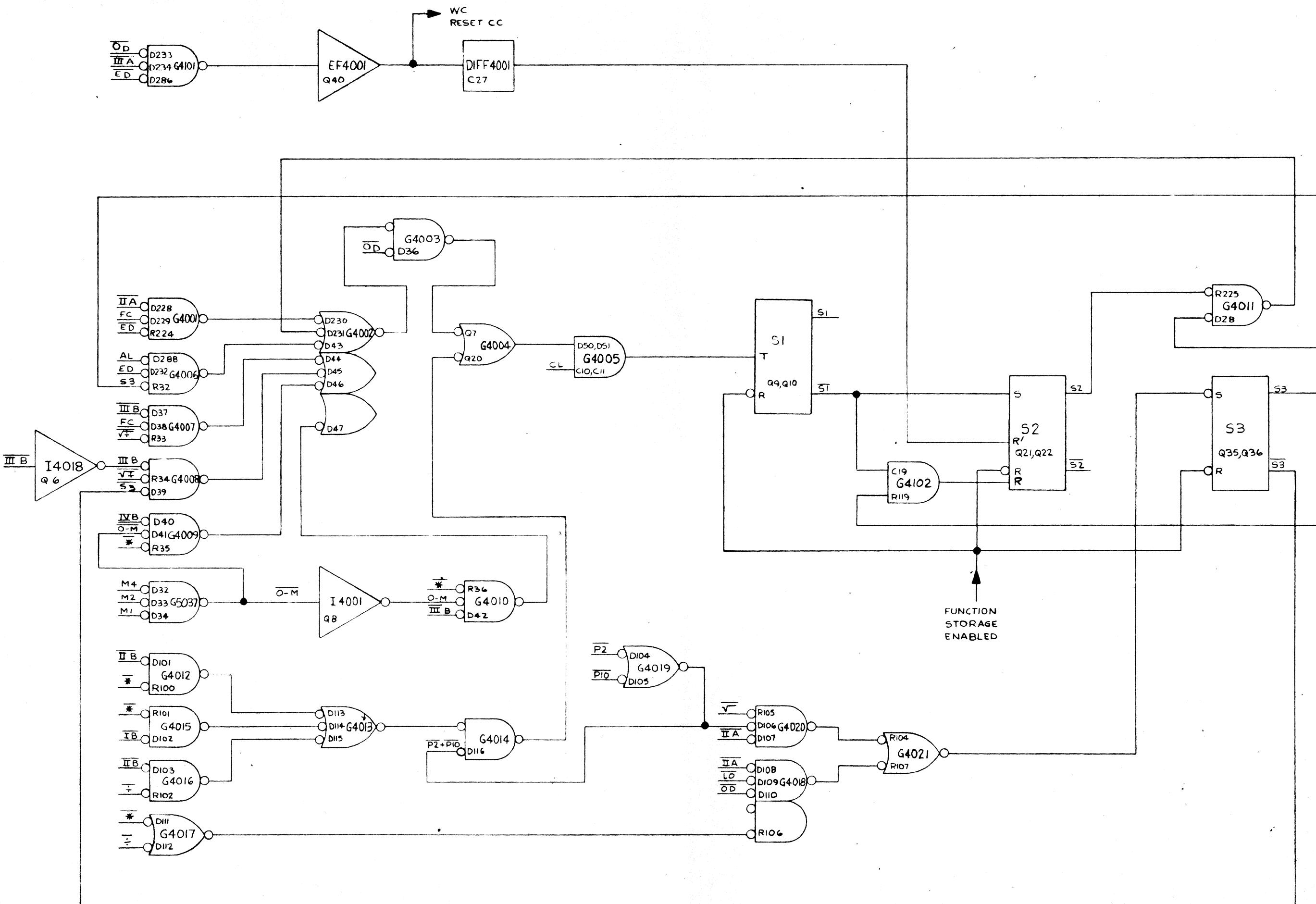


FIG. 6-20  
CYCLE COUNTER

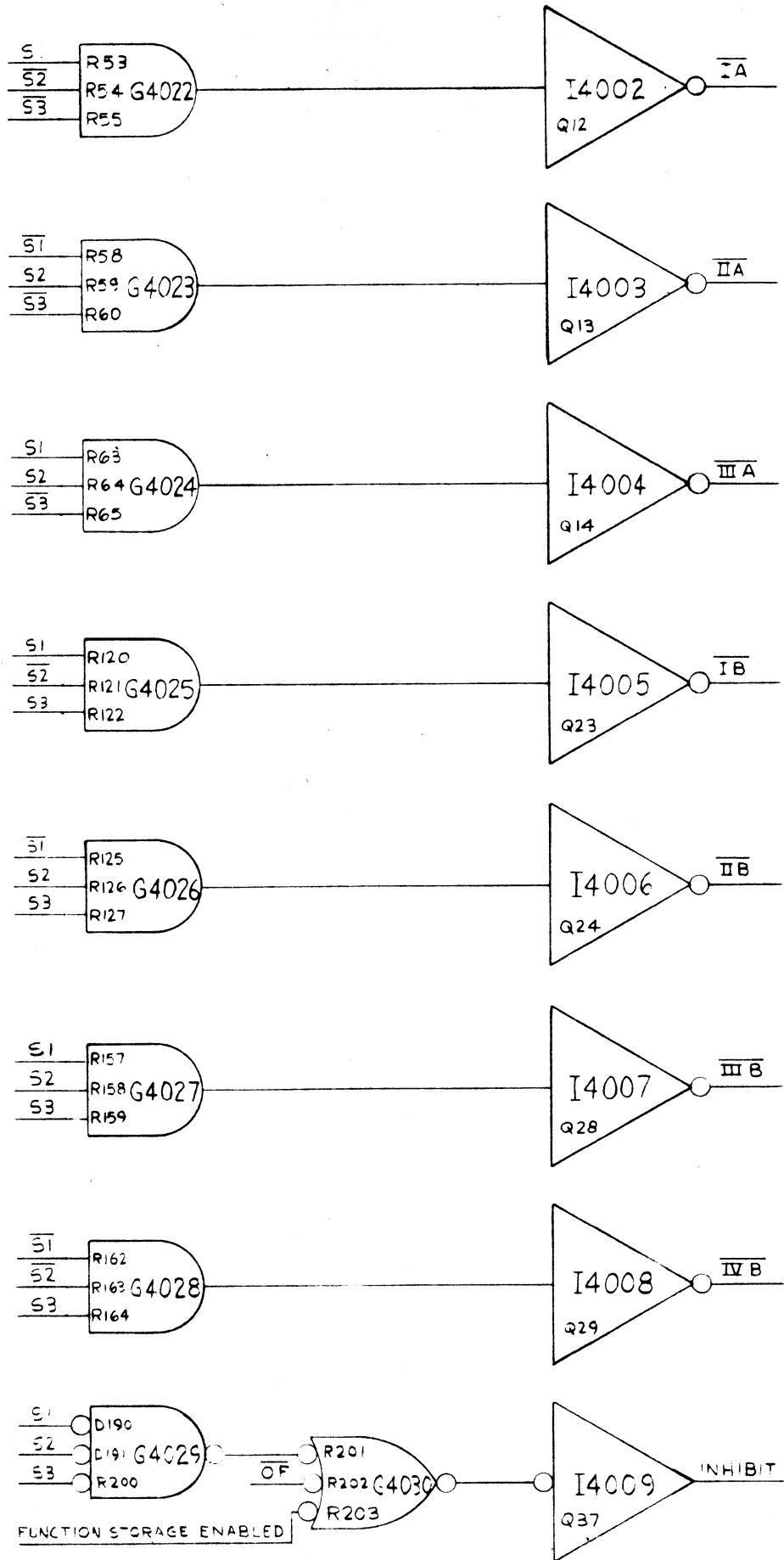


FIG. 6-21: CYCLE COUNT DECODER  
AND INHIBIT LOGIC

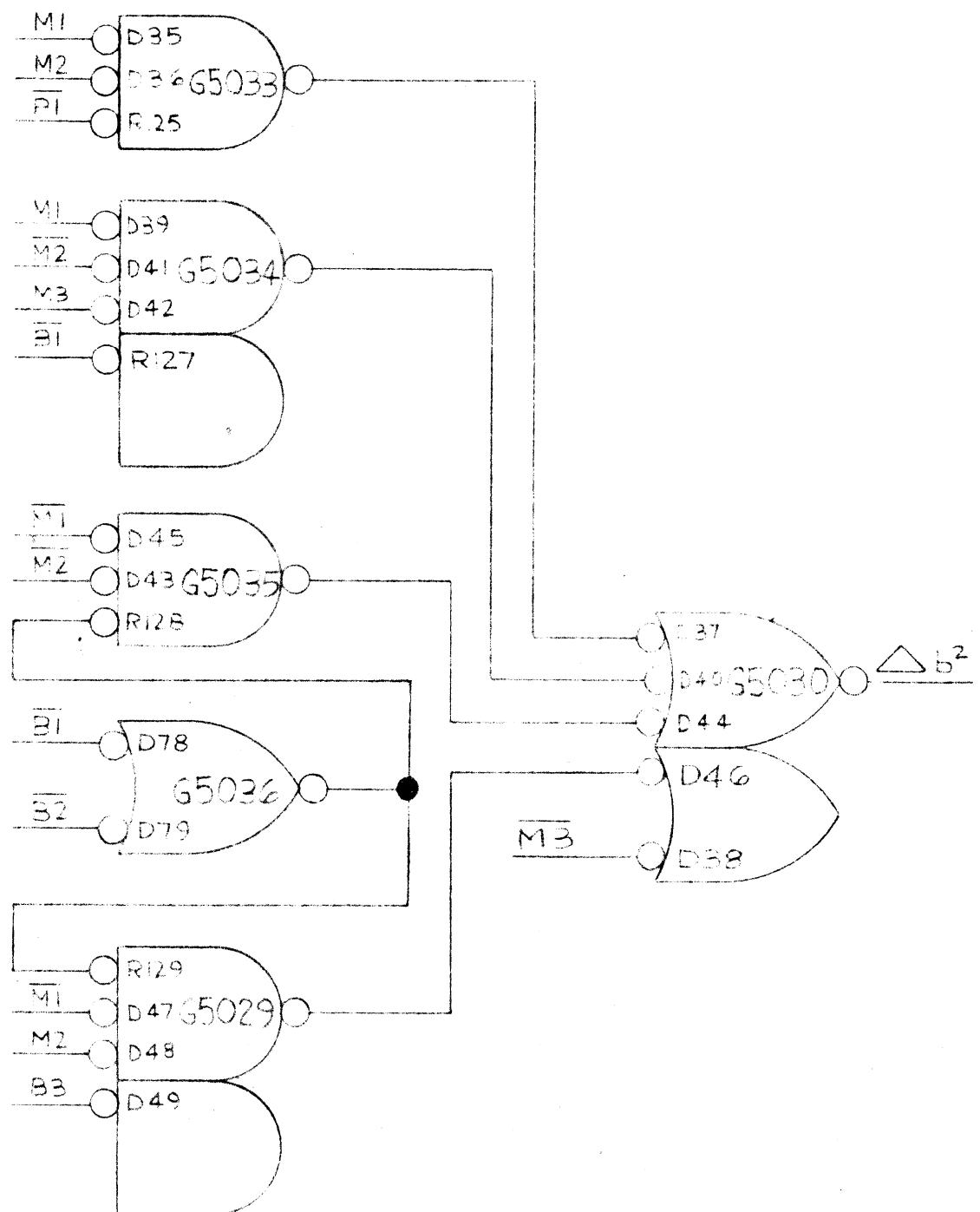


FIG. 6-22 :  $\Delta B^2$  GENERATOR

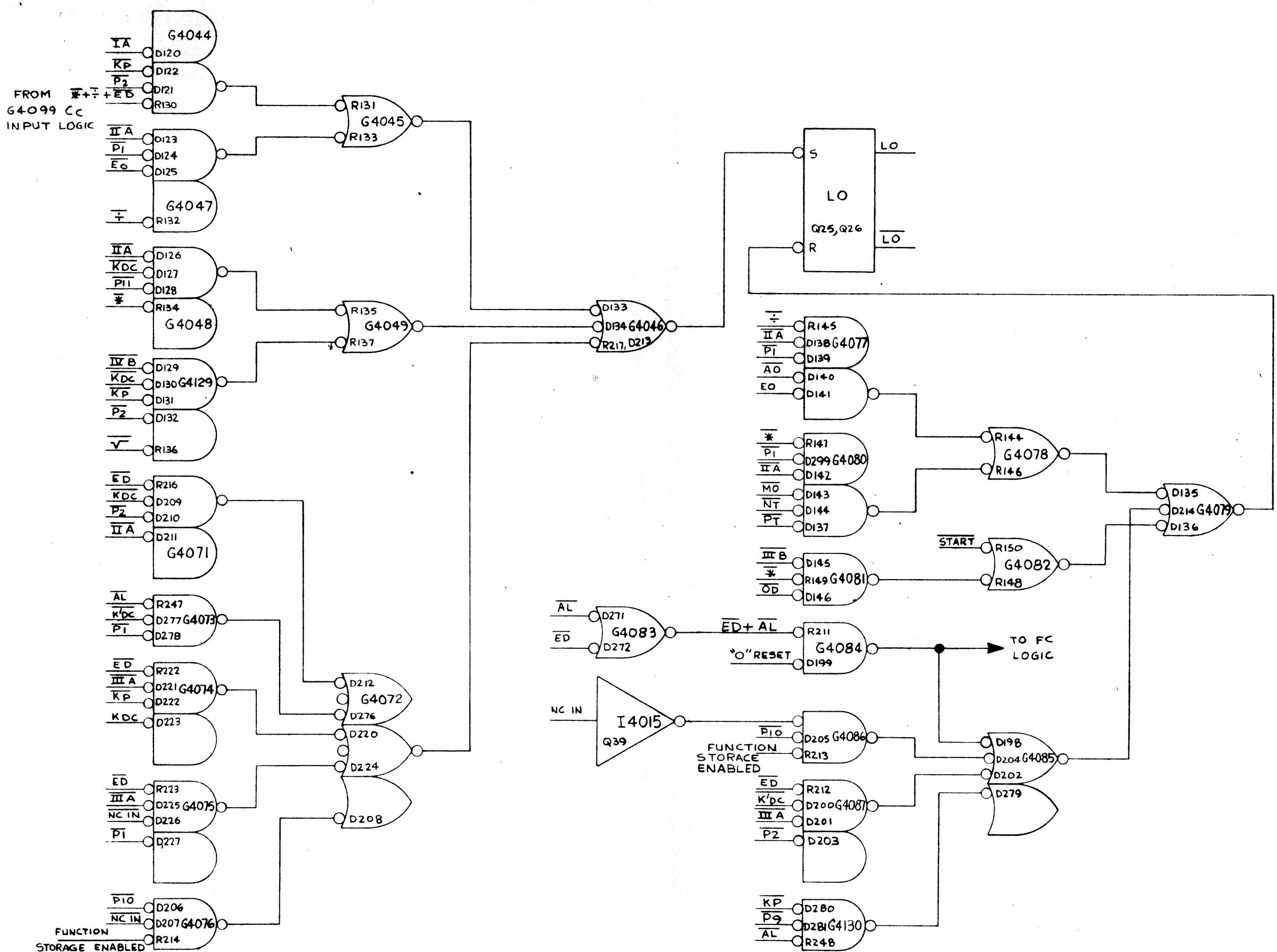


FIG. 6-23: LO LOGIC

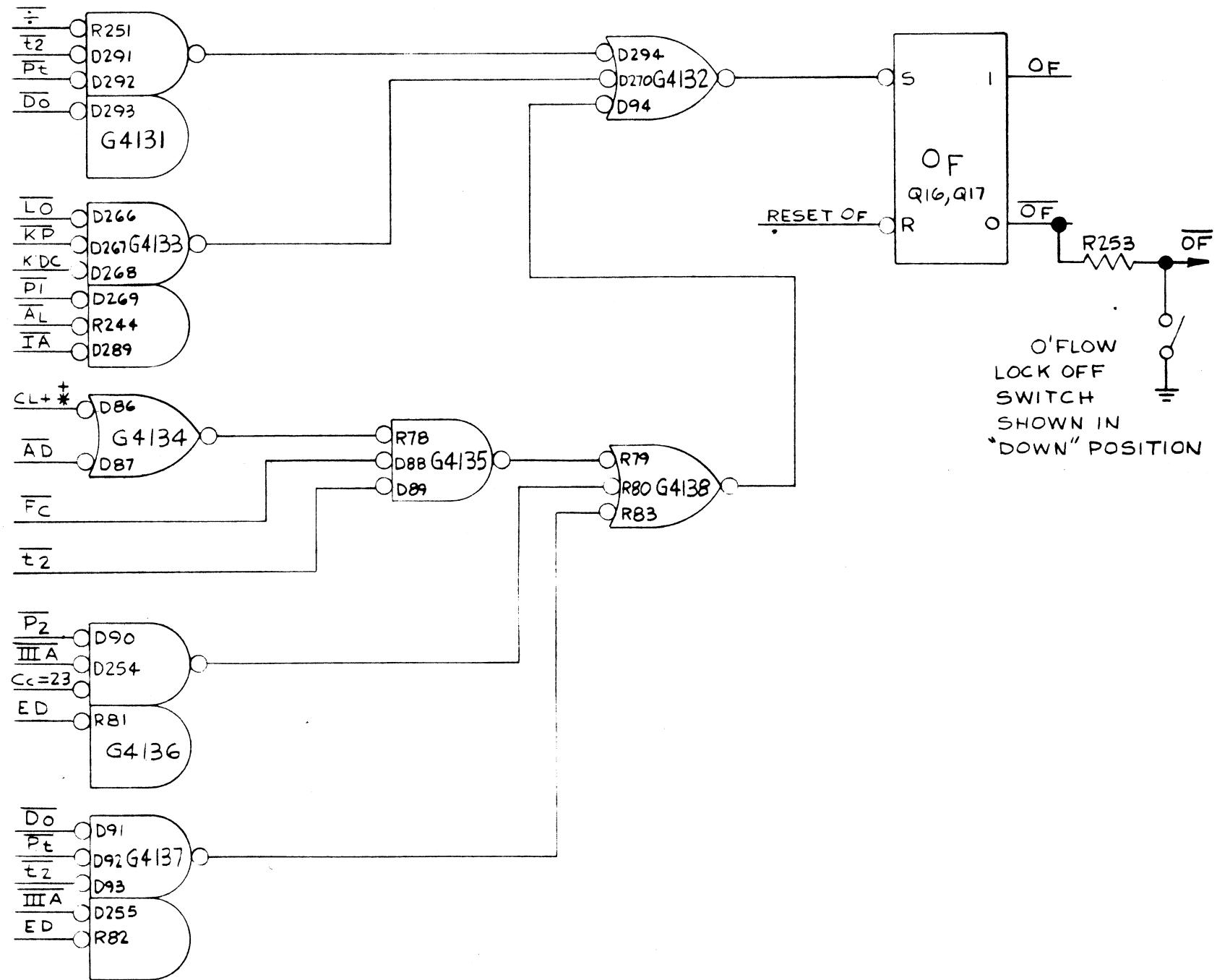


FIG. 6-24  
OF LOGIC

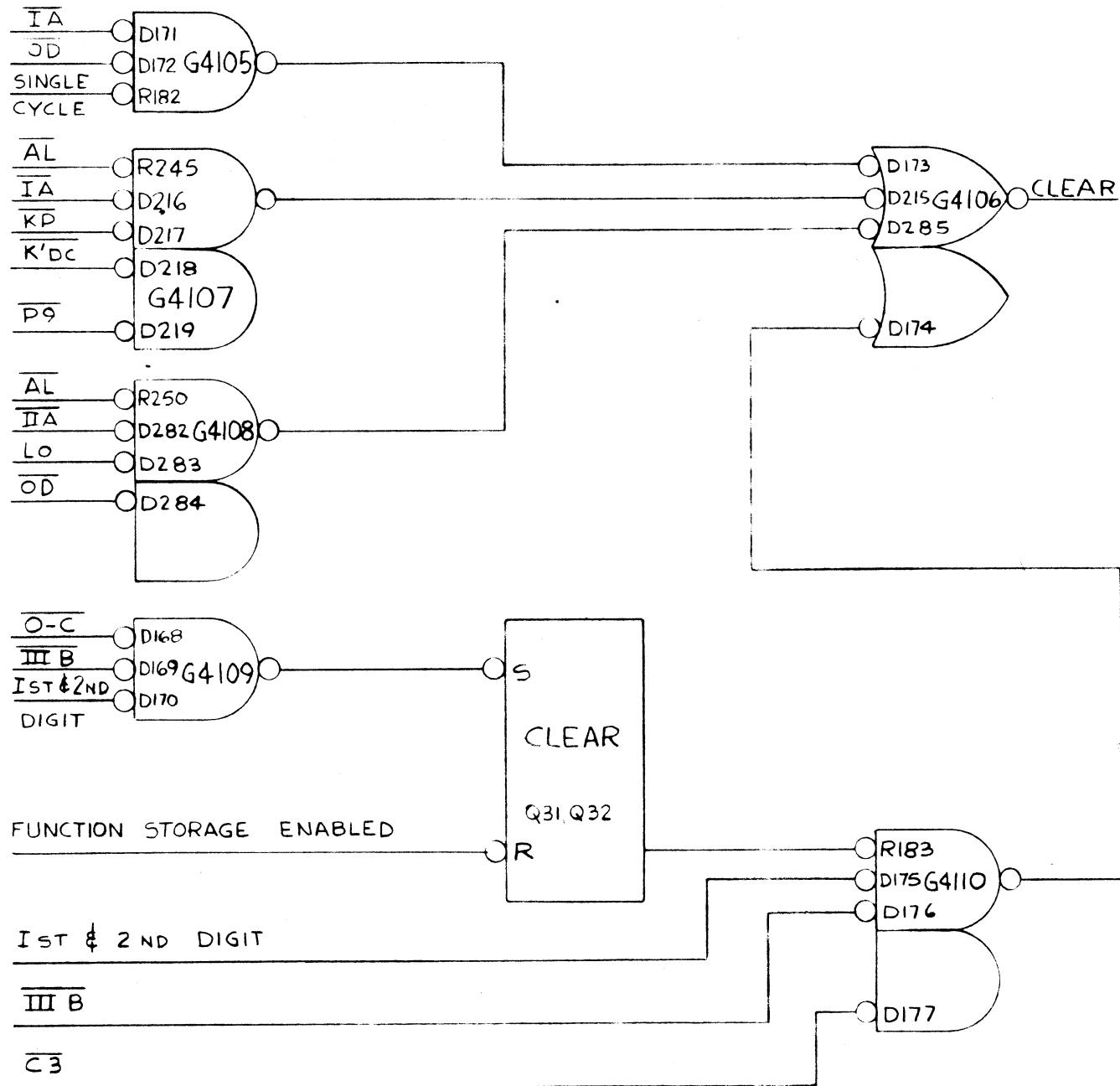


FIG. 6-25: CLEAR LOGIC

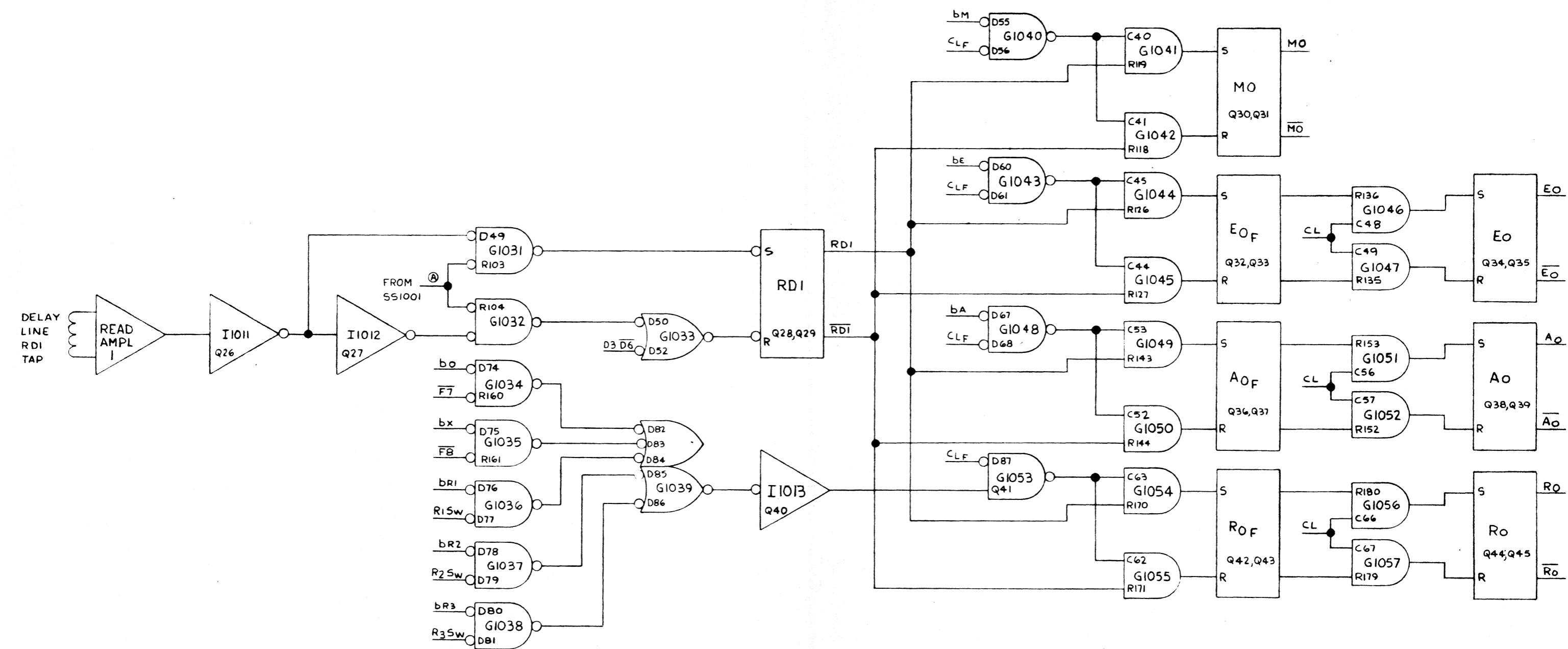


FIG 6-26  
DEMULTIPLEXER LOGIC

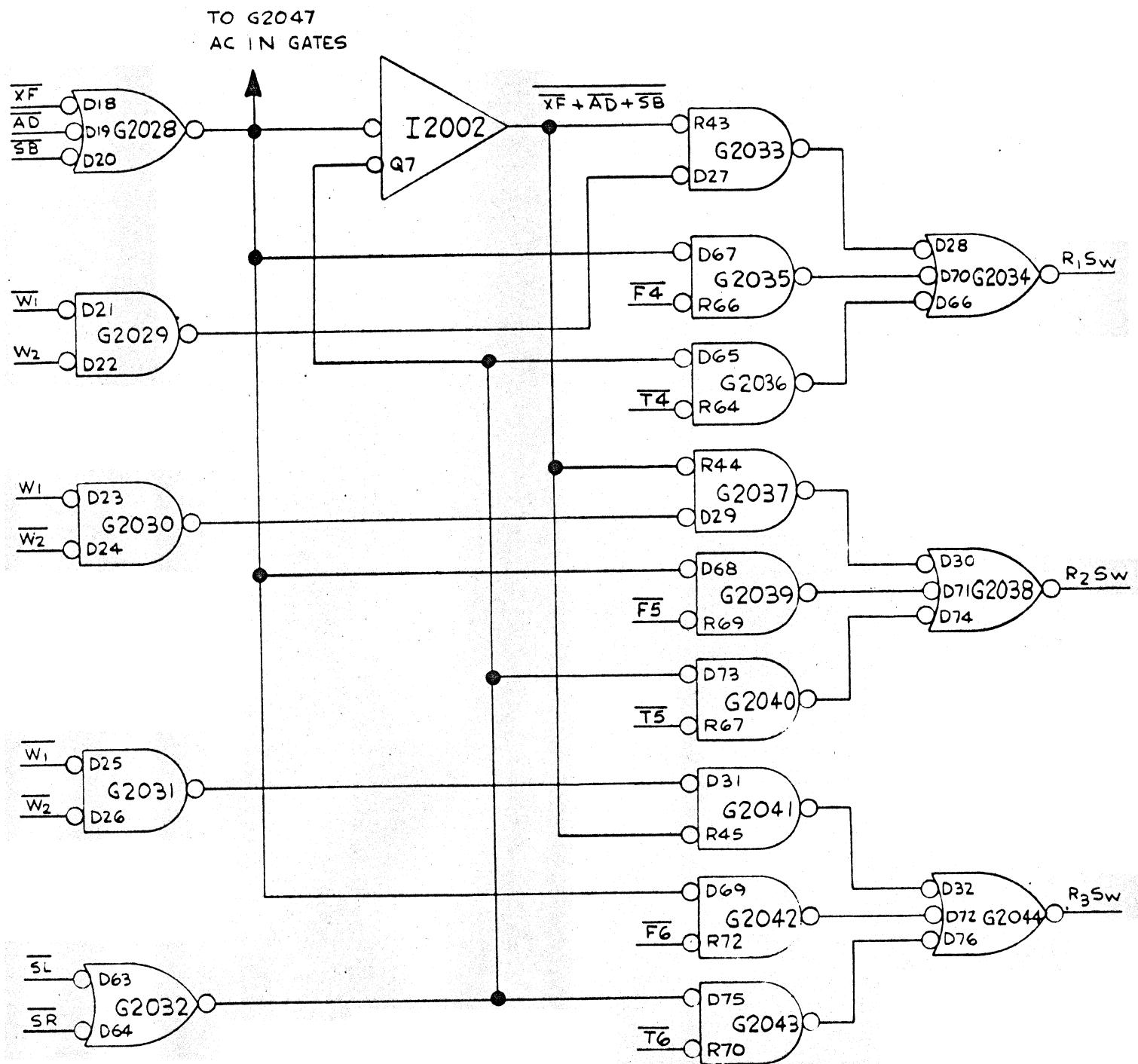


FIG. 6-27: R SW LOGIC

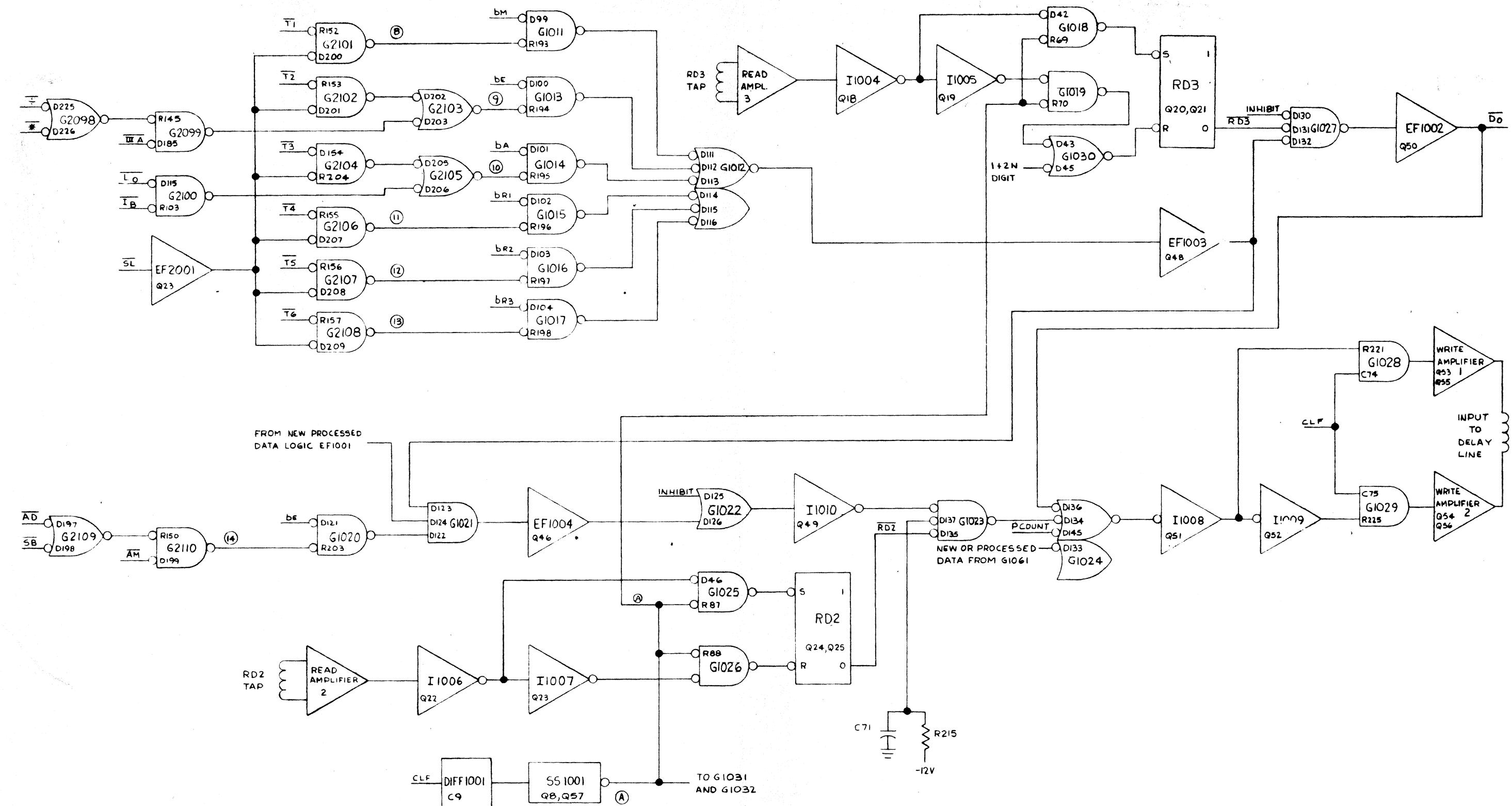
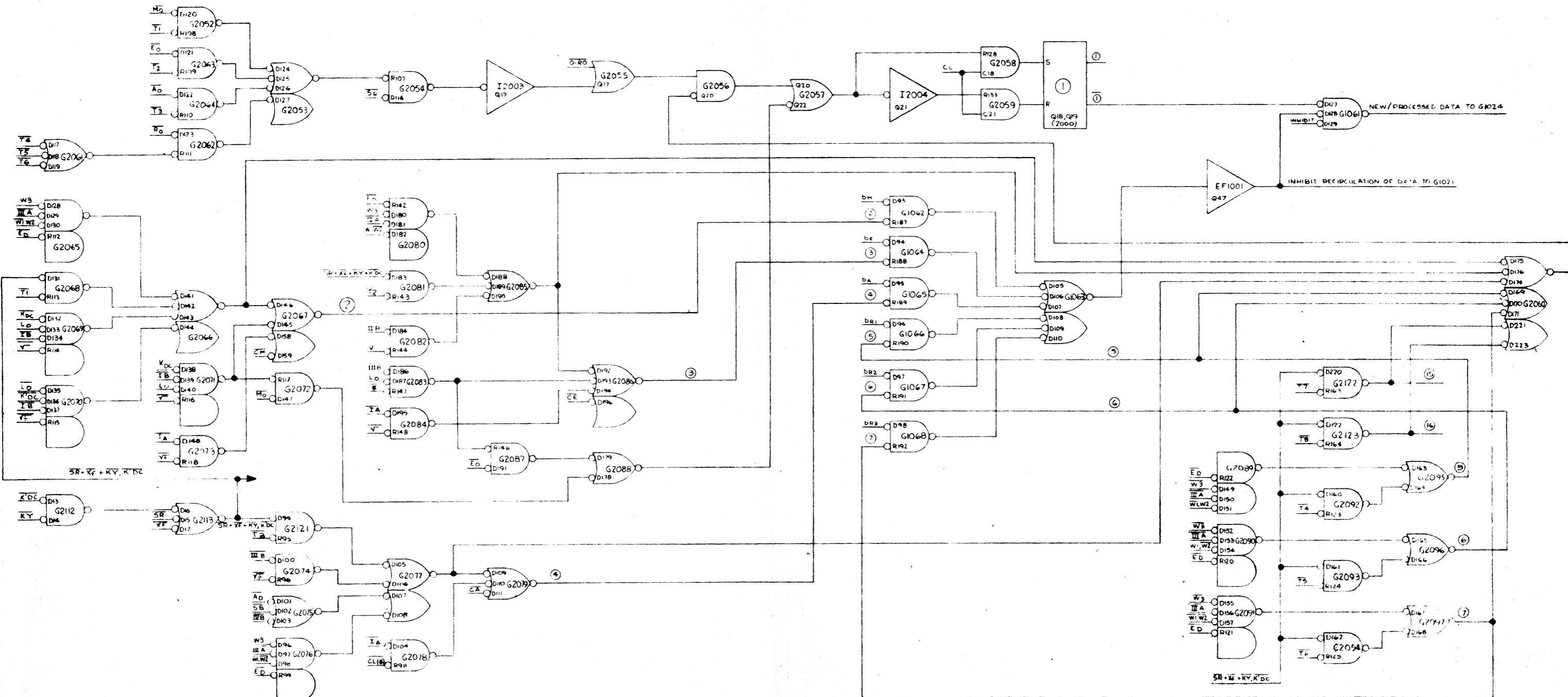


FIG. 6-28  
SHIFT LEFT, RECIRCULATE LOGIC



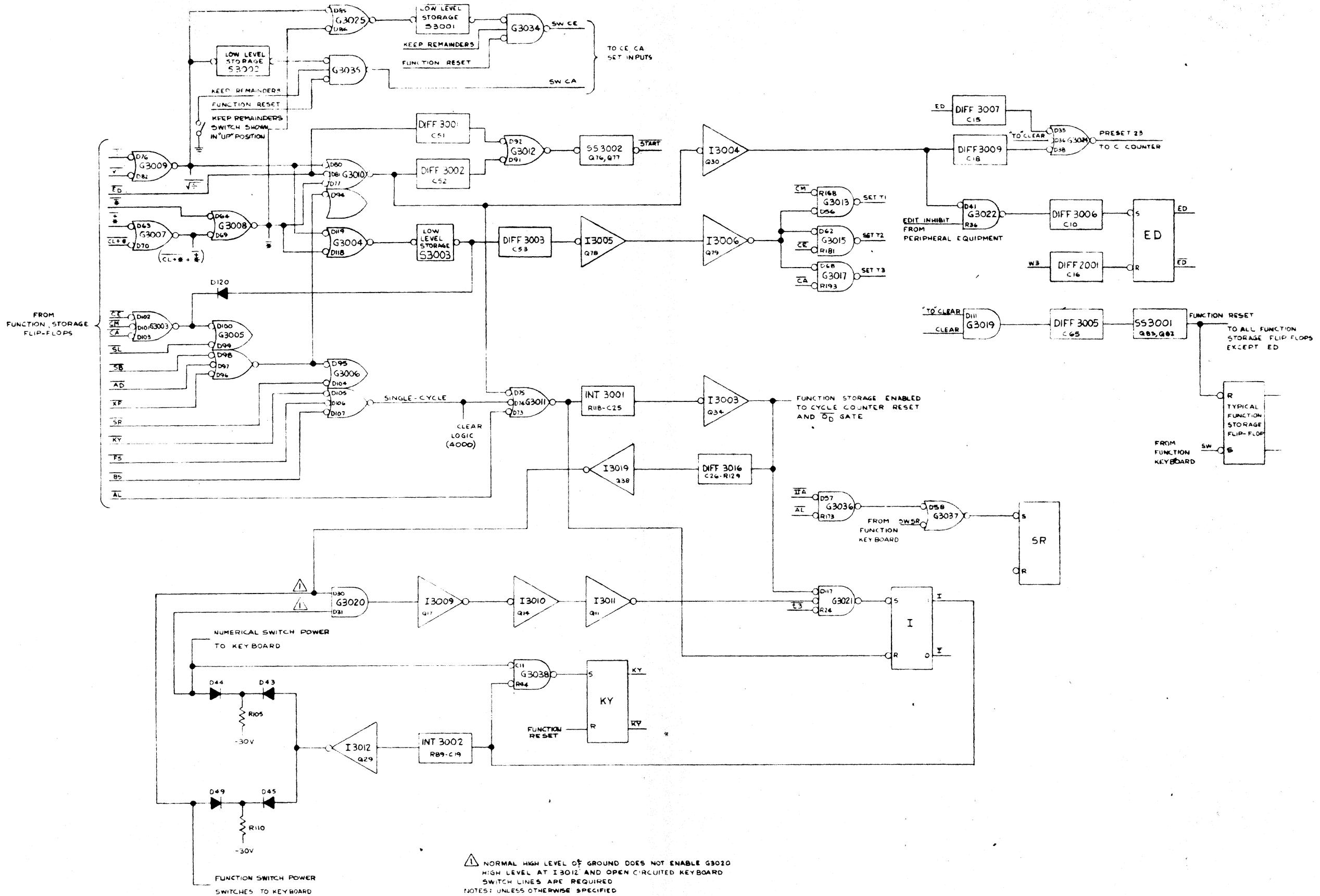


FIG. 6-30  
FUNCTION CONTROL LOGIC

FIG. 6-30

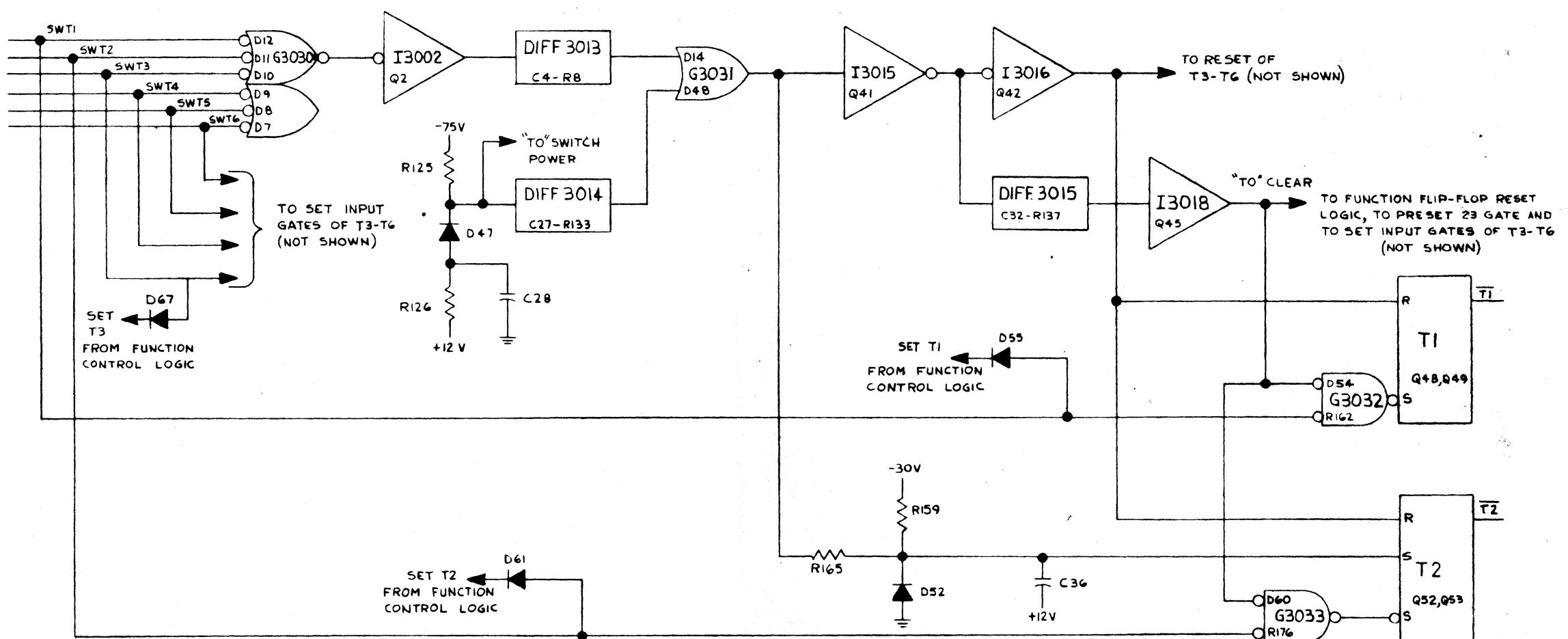
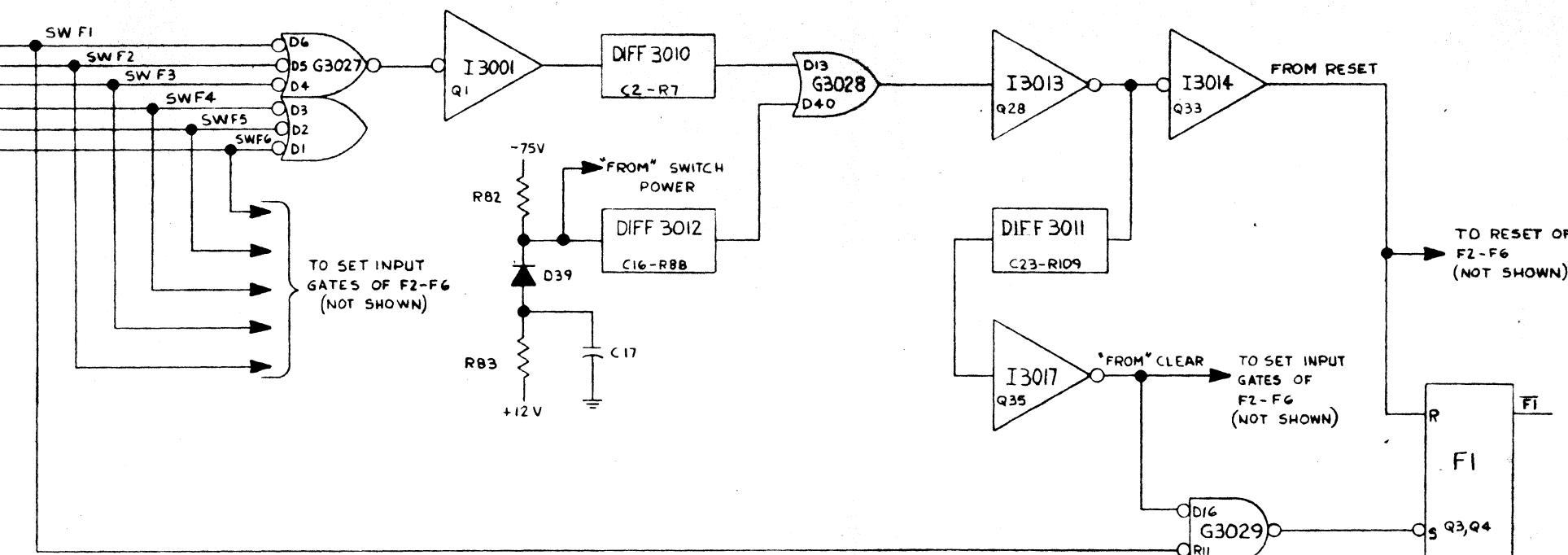


FIG. 6-31  
"TO"-FROM" STORAGE AND CONTROL LOGIC

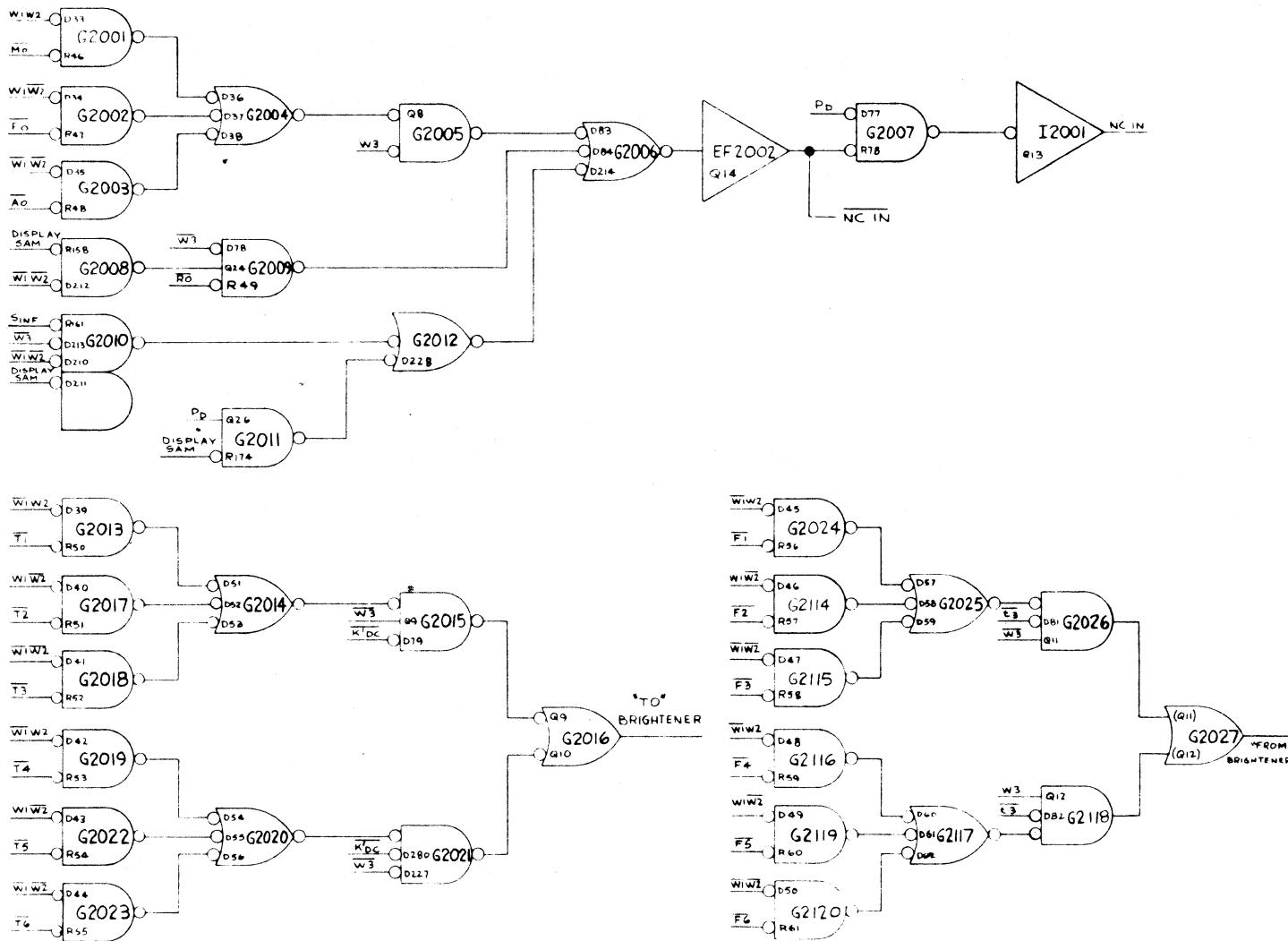


FIG. 6-32: NC INPUT, "TO" BRIGHTENER, "FROM" BRIGHTENER LOGIC

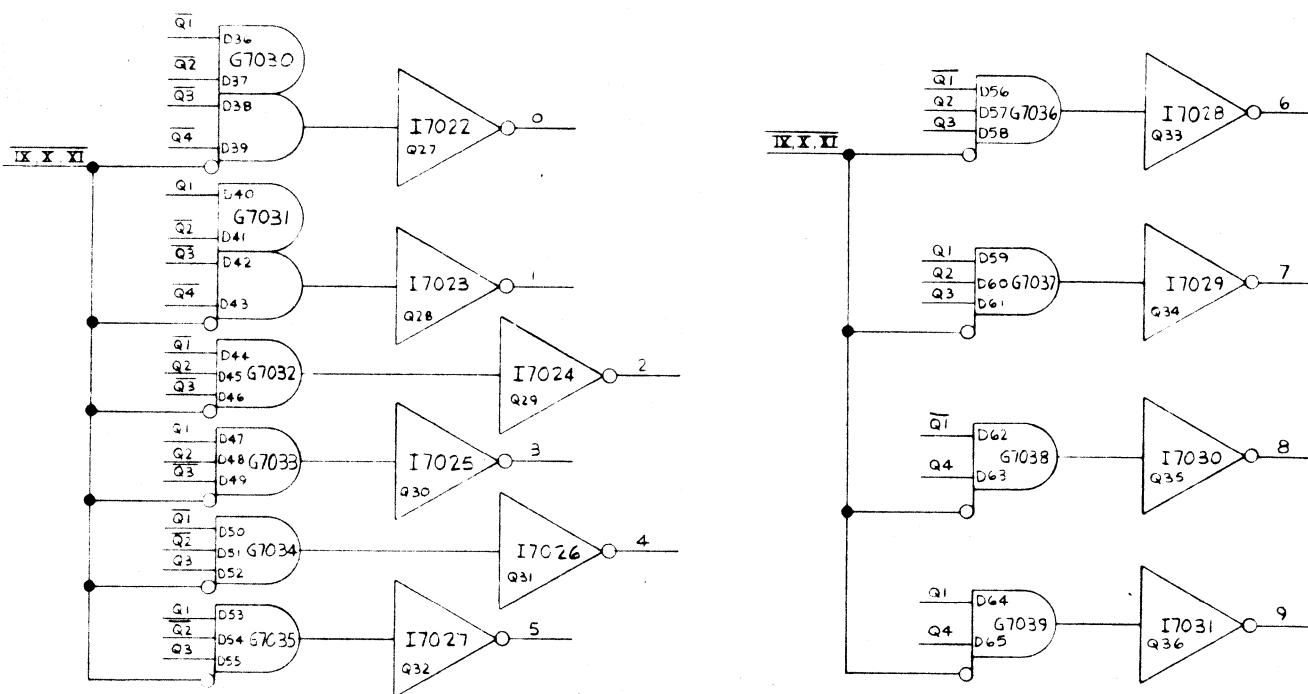
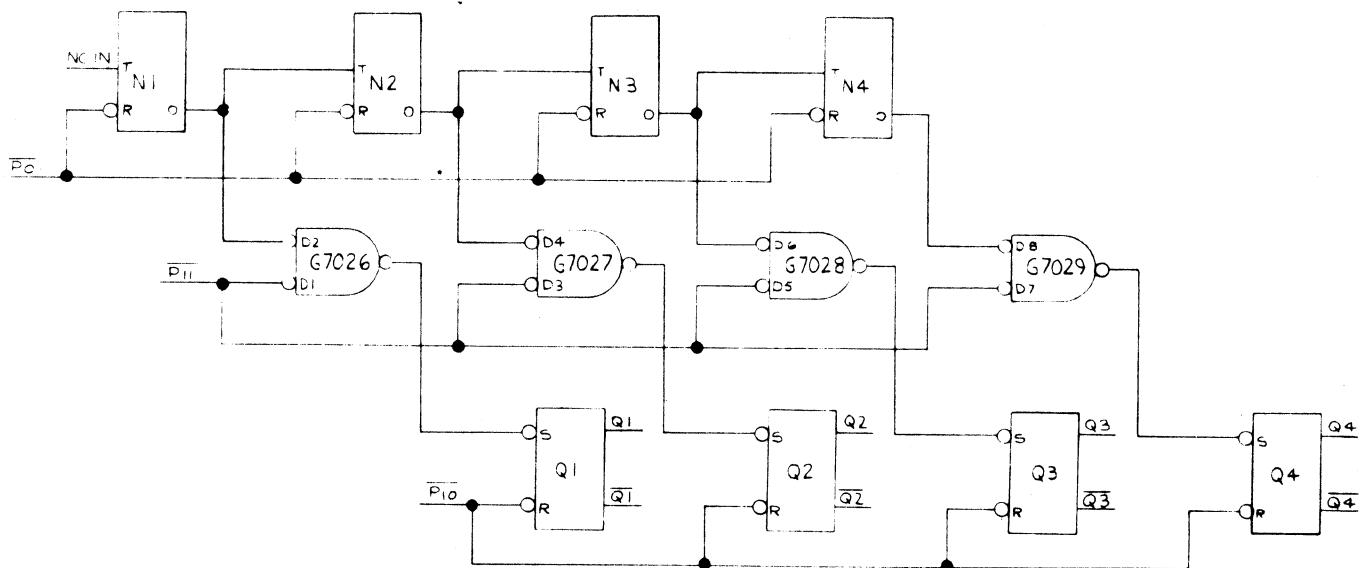


FIG. 6-33: CHARACTER (N) COUNTER STORAGE (Q) REGISTER,  
AND DECODER LOGIC

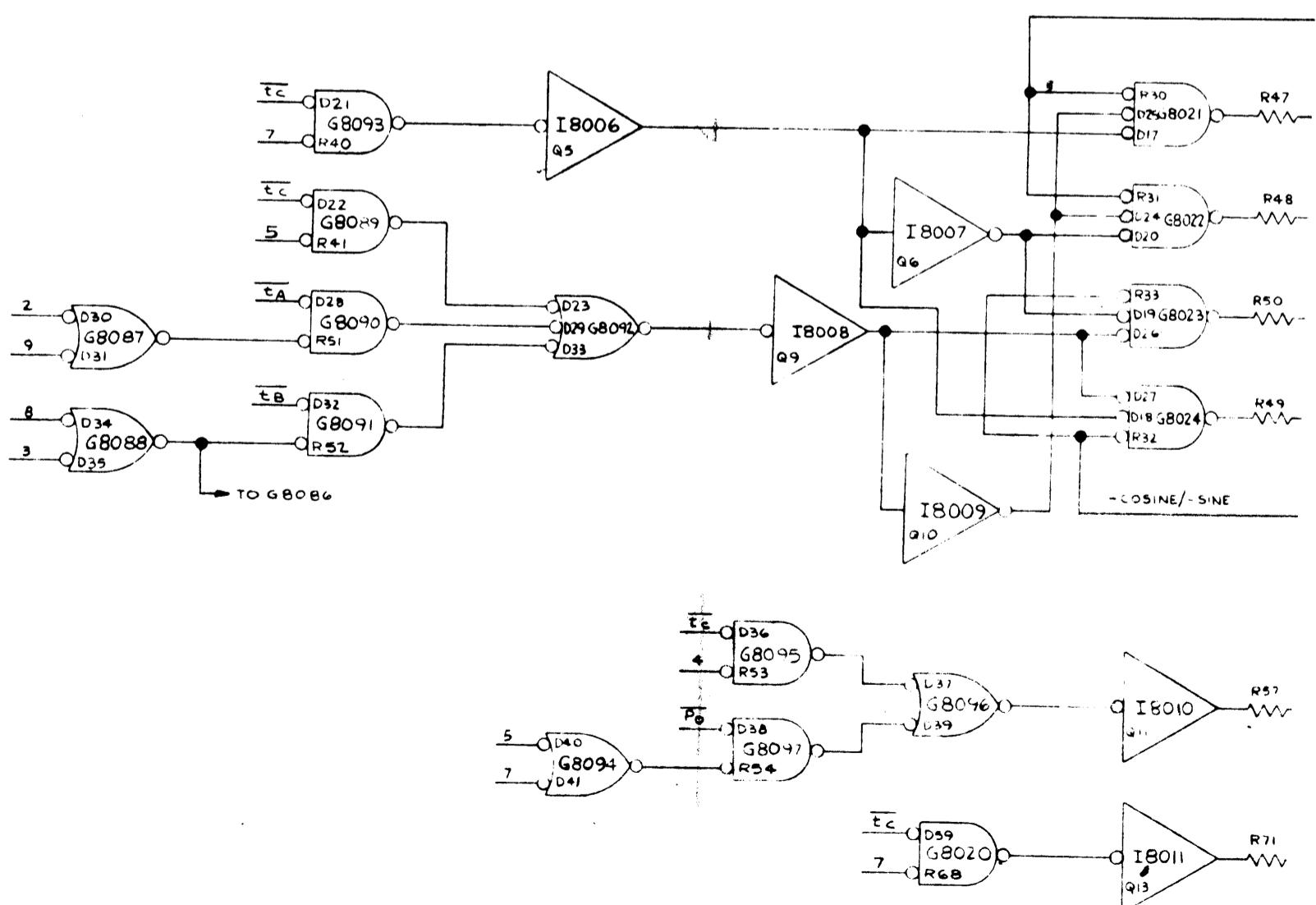
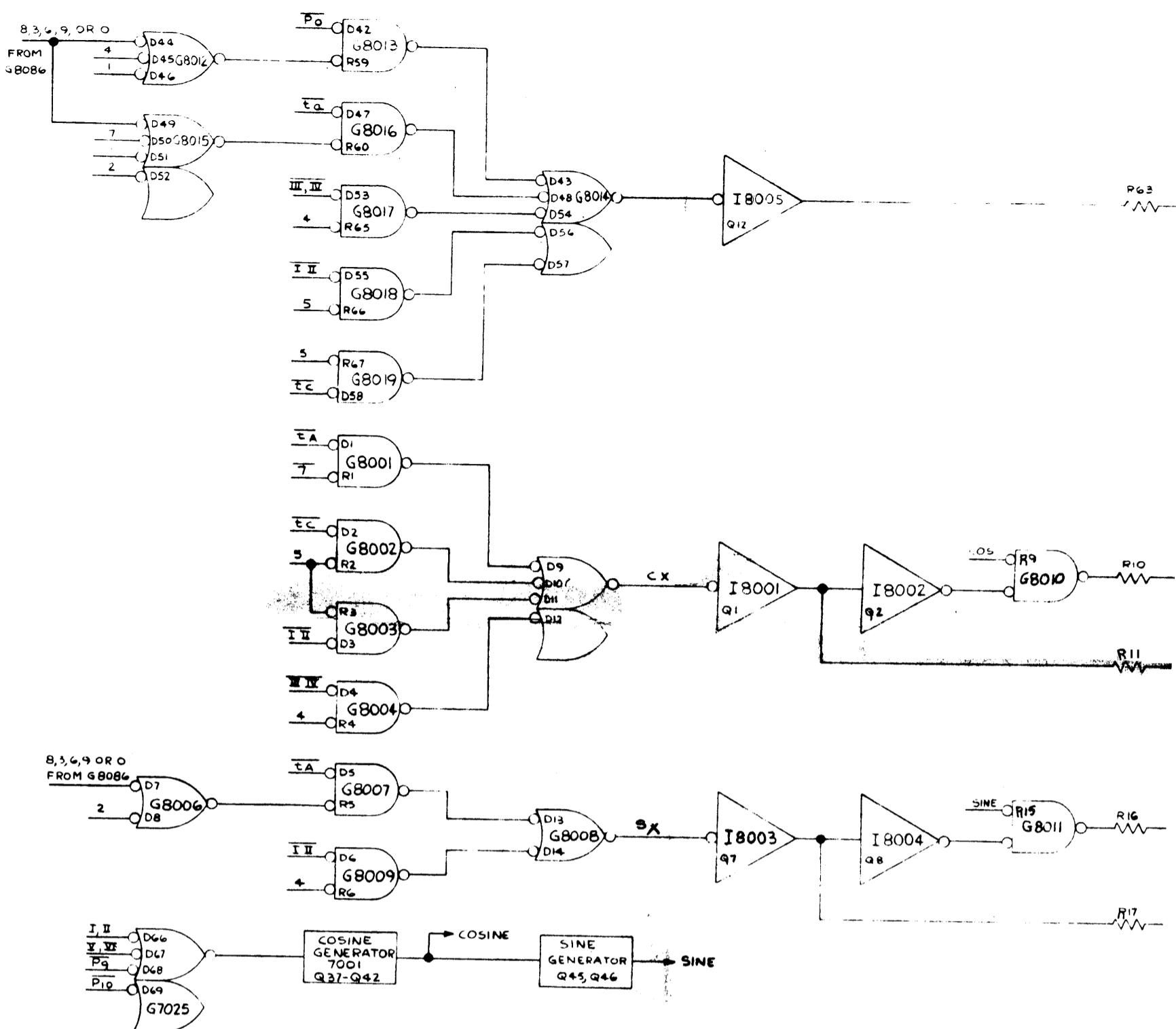


FIG 6-34 OPTICAL CODER

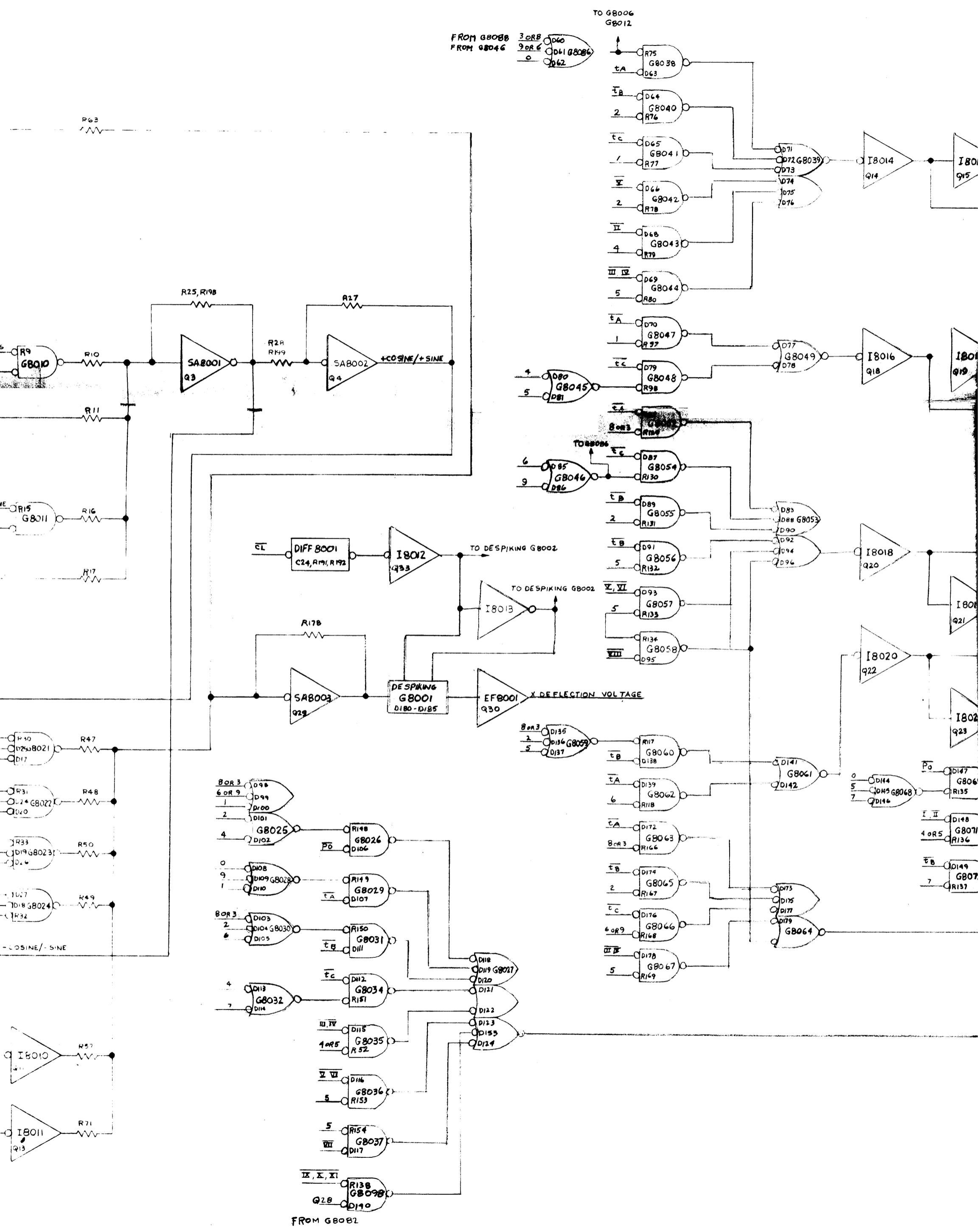
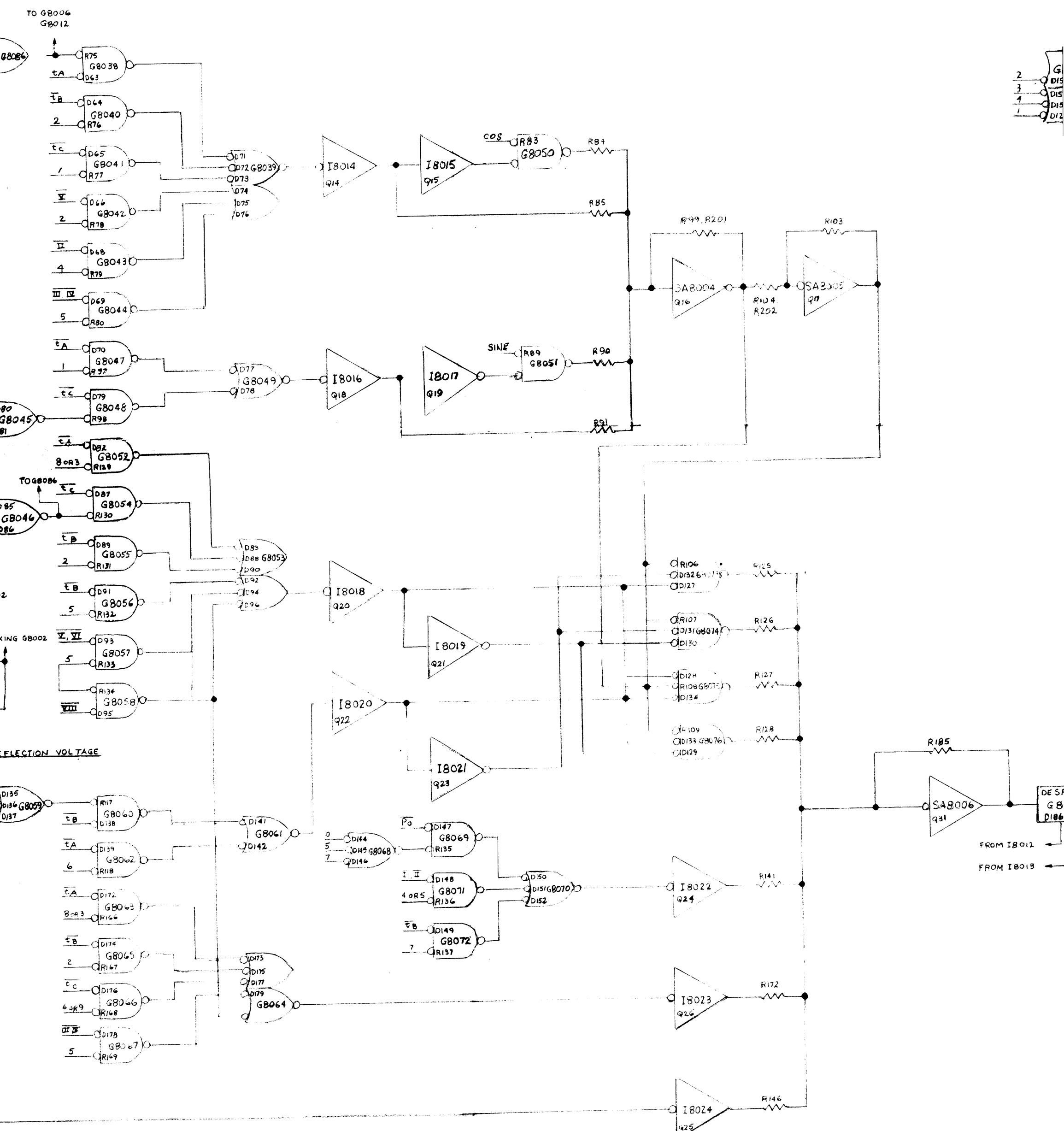
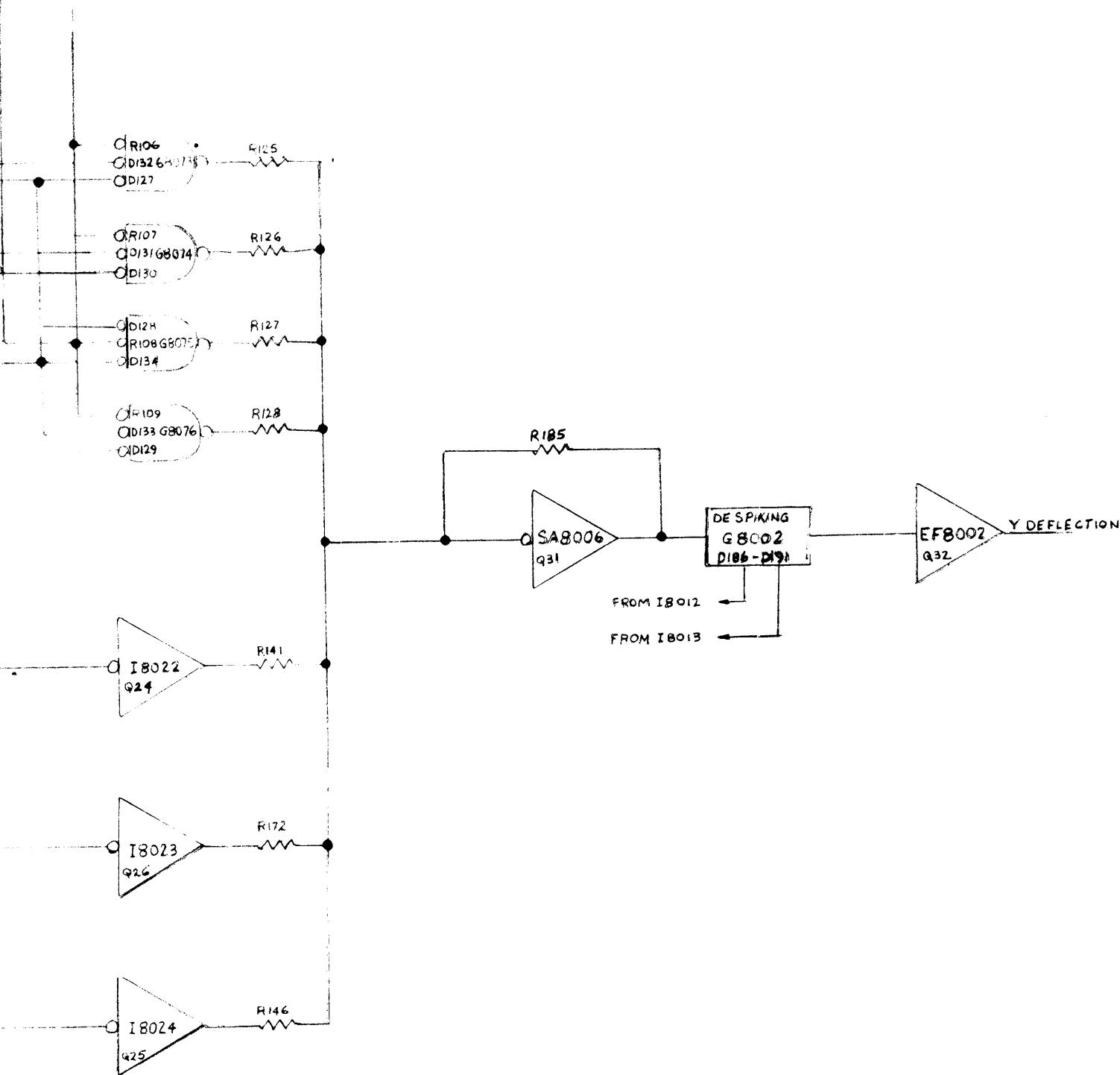
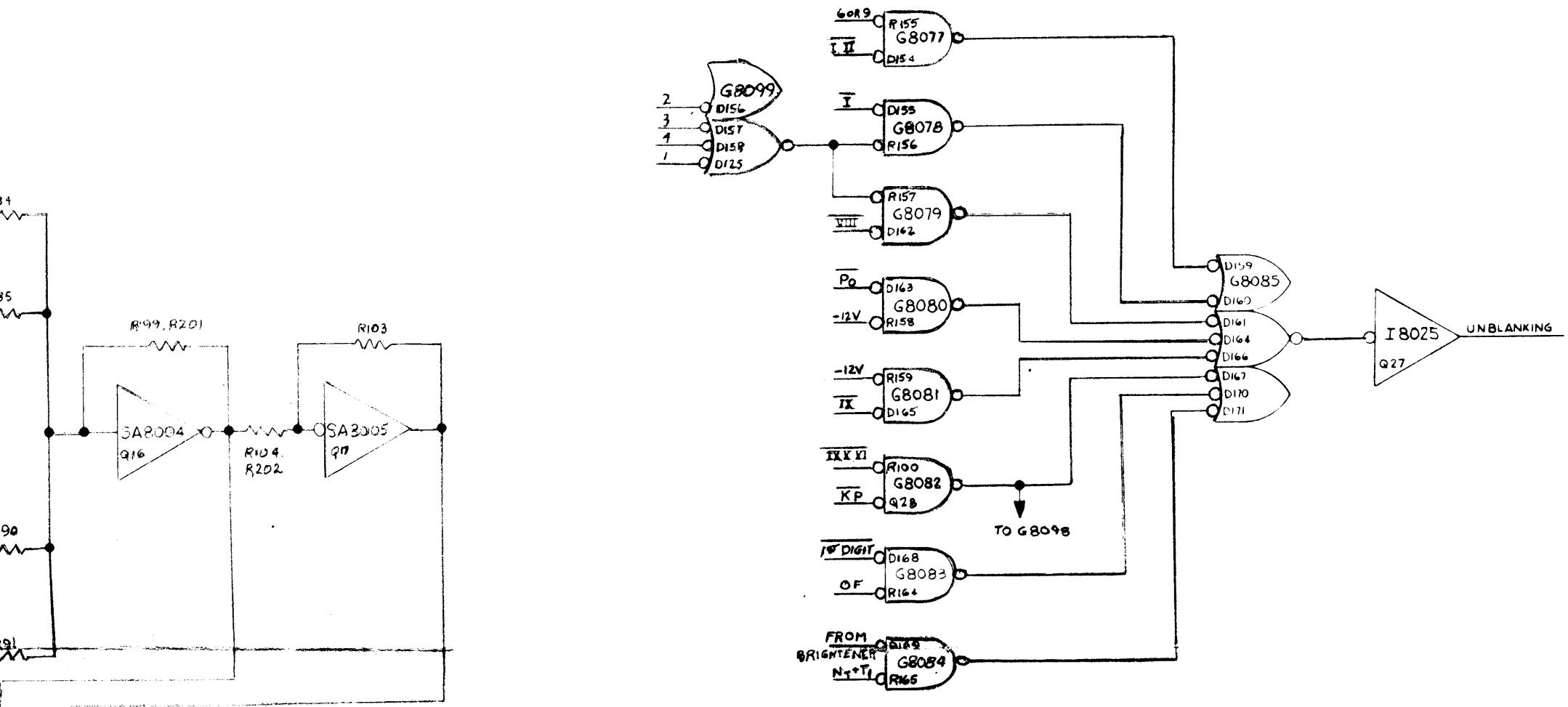


FIG. 6-34: OPTICAL CODER





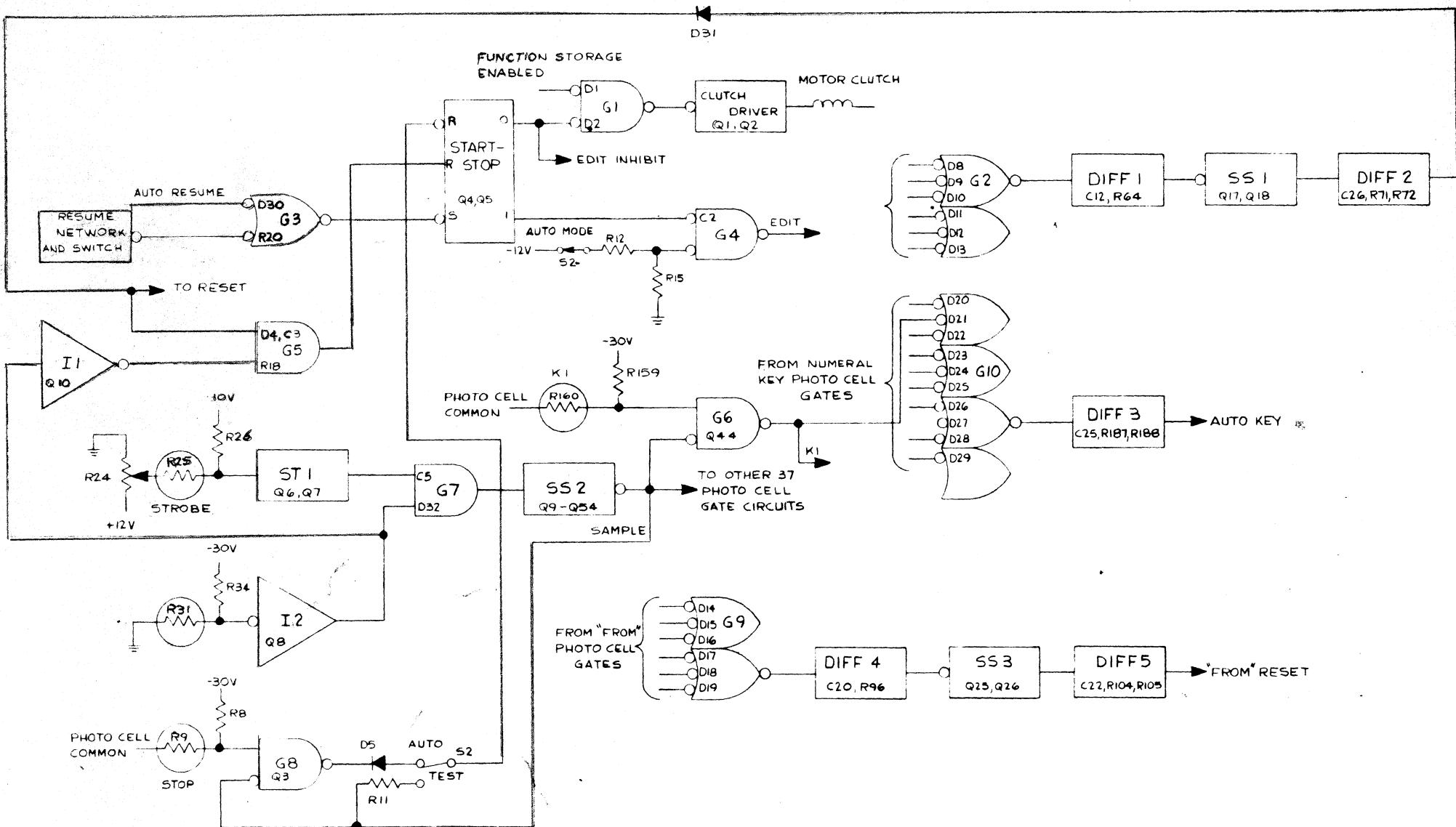


FIG. 6-35 CARD READER

REVISIONS			
LTR	DESCRIPTION	ECN	DRN
A	PRODUCTION RELEASE	23643	11/13/66

D

D

C

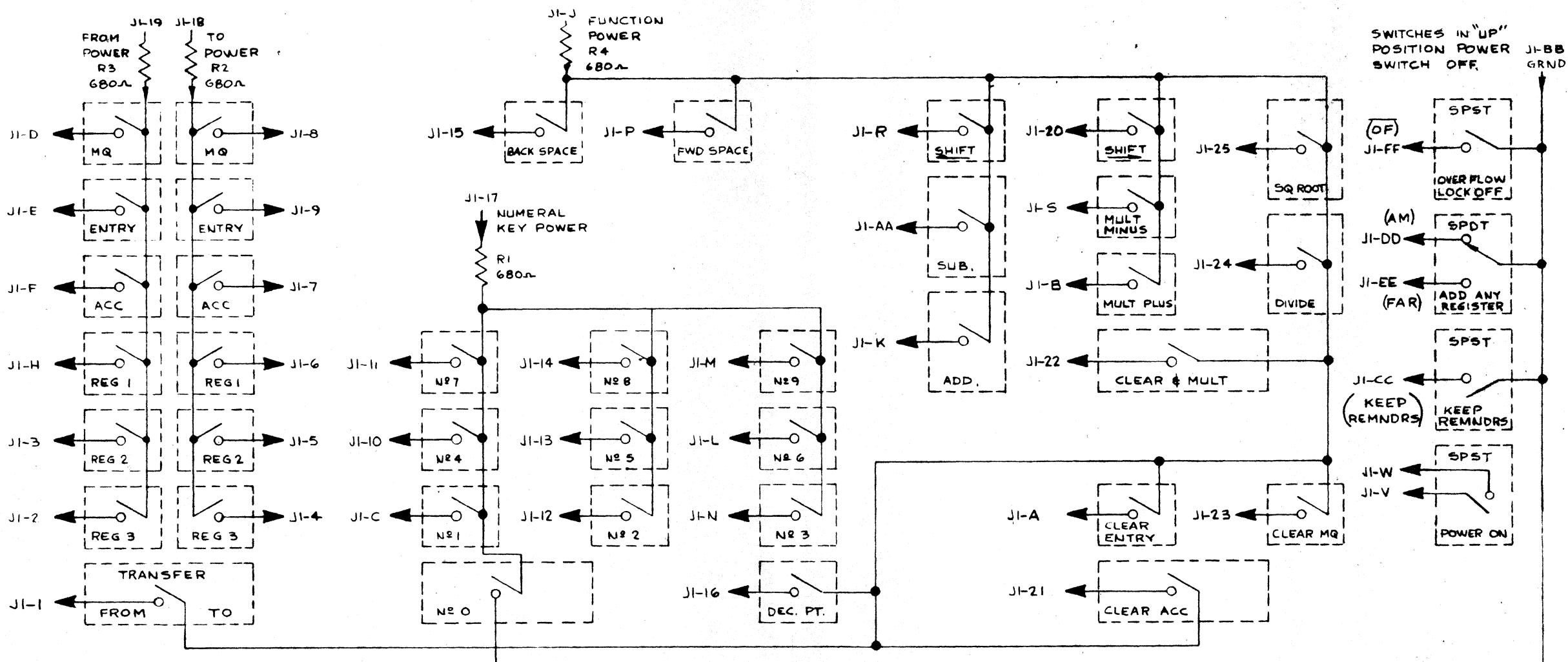
C

B

B

A

A



1.  
NOTES: UNLESS OTHERWISE SPECIFIED

MATERIAL DESCRIPTION	MATERIAL SPECIFICATION	FIRST USED ON	SIMILAR TO	NEXT ASSEM.	MODEL
UNLESS OTHERWISE SPECIFIED TOLERANCES AND NOTES LINEAR XXX - ±.010 ANGULAR ±1/2° XX - ±.03	THIS DOCUMENT CONTAINS PROPRIETARY INFORMATION WHICH MAY NOT BE DISCLOSED TO OTHERS OR FOR ANY PURPOSE OR USED FOR MANUFACTURING PURPOSES WITHOUT WRITTEN PERMISSION FROM WYLE LABORATORIES.	SCALE NAME DATE	DRAWN BY 1: 4/21/66	CHKD BY CR Muller	PROJ ENGR 4/21/66
1. DO NOT SCALE THE DRAWING 2. ALL DIMENSIONS ARE IN INCHES 3. DIMENSIONS APPLY AFTER FINISHING AND HEAT TREATMENT 4. BREAK ALL SHARP EDGES .010 R. APPROX.	WYLE LABORATORIES PRODUCTS DIVISION EL SEGUNDO, CALIF.	TITLE KEYBOARD - D.L. CALCULATOR	DRAWING NO. C12025 FIG. 6-36		